Pioneer sound.vision.soul

Service Manual



ORDER NO. ARP3281

MEDIA RECEIVER

PDP-R06G PDP-R06C

THIS MANUAL IS APPLICABLE TO THE FOLLOWING MODEL(S) AND TYPE(S).

Model	Туре	Power Requirement	Remarks
PDP-R06G	TLDFXJ	AC 110V - 240V	
PDP-R06C	WAXU5	AC 220V - 240V	



For details, refer to "Important Check Points for good servicing".

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SAFETY INFORMATION



This service manual is intended for qualified service technicians; it is not meant for the casual do-ityourselfer. Qualified technicians have the necessary test equipment and tools, and have been trained to properly and safely repair complex products such as those covered by this manual.

Improperly performed repairs can adversely affect the safety and reliability of the product and may void the warranty. If you are not qualified to perform the repair of this product properly and safely, you should not risk trying to do so and refer the repair to a qualified service technician.

WARNING

This product contains lead in solder and certain electrical parts contain chemicals which are known to the state of California to cause cancer, birth defects or other reproductive harm.

Health & Safety Code Section 25249.6 - Proposition 65

NOTICE

(FOR CANADIAN MODEL ONLY)

Fuse symbols— (fast operating fuse) and/or - (slow operating fuse) on PCB indicate that replacement parts must be of identical designation.

REMARQUE

(POUR MODÈLE CANADIEN SEULEMENT)

Les symboles de fusible (fusible de type rapide) et/ou (fusible de type lent) sur CCI indiquent que les pièces de remplacement doivent avoir la même désignation.

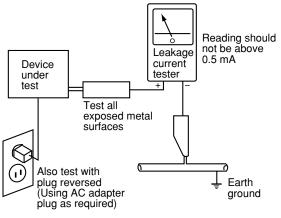
(FOR USA MODEL ONLY)

1. SAFETY PRECAUTIONS

The following check should be performed for the continued protection of the customer and service technician.

LEAKAGE CURRENT CHECK

Measure leakage current to a known earth ground (water pipe, conduit, etc.) by connecting a leakage current tester such as Simpson Model 229-2 or equivalent between the earth ground and all exposed metal parts of the appliance (input/output terminals, screwheads, metal overlays, control shaft, etc.). Plug the AC line cord of the appliance directly into a 120V AC 60 Hz outlet and turn the AC power switch on. Any current measured must not exceed 0.5 mA.



AC Leakage Test

ANY MEASUREMENTS NOT WITHIN THE LIMITS OUTLINED ABOVE ARE INDICATIVE OF A POTENTIAL SHOCK HAZARD AND MUST BE CORRECTED BEFORE RETURNING THE APPLIANCE TO THE CUSTOMER.

2. PRODUCT SAFETY NOTICE

Many electrical and mechanical parts in the appliance have special safety related characteristics. These are often not evident from visual inspection nor the protection afforded by them necessarily can be obtained by using replacement components rated for voltage, wattage, etc. Replacement parts which have these special safety characteristics are identified in this Service Manual.

Electrical components having such features are identified by marking with a \triangle on the schematics and on the parts list in this Service Manual.

The use of a substitute replacement component which does not have the same safety characteristics as the PIONEER recommended replacement one, shown in the parts list in this Service Manual, may create shock, fire, or other hazards.

Product Safety is continuously under review and new instructions are issued from time to time. For the latest information, always consult the current PIONEER Service Manual. A subscription to, or additional copies of, PIONEER Service Manual may be obtained at a nominal charge from PIONEER.

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In this manual, procedures that must be performed during repairs are marked with the below symbol. Please be sure to confirm and follow these procedures.

Product safety



Please conform to product regulations (such as safety and radiation regulations), and maintain a safe servicing environment by following the safety instructions described in this manual.

1) Use specified parts for repair.

Use genuine parts. Be sure to use important parts for safety.

② Do not perform modifications without proper instructions.

Please follow the specified safety methods when modification(addition/change of parts) is required due to interferences such as radio/TV interference and foreign noise.

3 Make sure the soldering of repaired locations is properly performed.

When you solder while repairing, please be sure that there are no cold solder and other debris. Soldering should be finished with the proper quantity. (Refer to the example)

4 Make sure the screws are tightly fastened.

Please be sure that all screws are fastened, and that there are no loose screws.

⑤ Make sure each connectors are correctly inserted.

Please be sure that all connectors are inserted, and that there are no imperfect insertion.

6 Make sure the wiring cables are set to their original state.

Please replace the wiring and cables to the original state after repairs. In addition, be sure that there are no pinched wires, etc.

Make sure screws and soldering scraps do not remain inside the product.

Please check that neither solder debris nor screws remain inside the product.

® There should be no semi-broken wires, scratches, melting, etc. on the coating of the power cord.

Damaged power cords may lead to fire accidents, so please be sure that there are no damages. If you find a damaged power cord, please exchange it with a suitable one.

There should be no spark traces or similar marks on the power plug.

When spark traces or similar marks are found on the power supply plug, please check the connection and advise on secure connections and suitable usage. Please exchange the power cord if necessary.

10 Safe environment should be secured during servicing.

When you perform repairs, please pay attention to static electricity, furniture, household articles, etc. in order to prevent injuries. Please pay attention to your surroundings and repair safely.

2. Adjustments



To keep the original performance of the products, optimum adjustments and confirmation of characteristics within specification. Adjustments should be performed in accordance with the procedures/instructions described in this manual.

3. Lubricants, Glues, and Replacement parts



Use grease and adhesives that are equal to the specified substance. Make sure the proper amount is applied.

4. Cleaning



For parts that require cleaning, such as optical pickups, tape deck heads, lenses and mirrors used in projection monitors, proper cleaning should be performed to restore their performances.

5. Shipping mode and Shipping screws



To protect products from damages or failures during transit, the shipping mode should be set or the shipping screws should be installed before shipment. Please be sure to follow this method especially if it is specified in this manual.

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PDP-R06G

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	CONTENTS	
	1. SPECIFICATIONS	5
	2. EXPLODED VIEWS AND PARTS LIST	6
Α	2.1 PACKING SECTION	6
^	2.2 EXTERIOR SECTION	8
	2.3 FRONT PANEL SECTION	
	3. BLOCK DIAGRAM	
	3.1 OVERALL BLOCK DIAGRAM	
	3.2 POWER SUPPLY UNIT	
	3.3 POWER SUPPLY SIGNAL ROUTE	
	3.4 VOLTAGES	16
	5. PCB PARTS LIST	
	6. ADJUSTMENT	
	6.1 POSSIBLE CASES WHERE READJUSTMENT IS REQUIRED	
	6.2 SERVICING USING ONLY THE MEDIA RECEIVER	
В	6.3 SERVICE FACTORY MODE	
	6.4 LIST OF RS-232C COMMANDS	
	6.5 OUTLINE OF COMMANDS	
	7. GENERAL INFORMATION	
	7.1 DIAGNOSIS	
	7.1.1 TROUBLESHOOTING	
	7.1.2 DISASSEMBLY	50
	7.2 EXPLANATION	51

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Item			Media Receiver, Model: PDP-R06G	
Color System			PAL/SECAM/NTSC/4.43NTSC/PAL-M/PAL-N	
TV Function	Receiving Sy	ystem	PAL: B/G, D/K, I M, N SECAM: B/G, D/K NTSC: M 4.43NTSC: M	
	Tuner	VHF/UHF	44.25-863.25 MHz	
		CATV	Hyper-band, S1-S41ch	
	Auto Channe	el Preset	99 ch (Normal), 68 ch (Air, US type), 125 ch (Cable, US type), Auto Preset	
	Audio multiplex		NICAM/A2/BTSC System	
Terminals	Rear	INPUT 1	COMPONENT VIDEO in, S-VIDEO in, AV in	
		INPUT 2	COMPONENT VIDEO in, S-VIDEO in, AV in	
		INPUT 3	S-VIDEO in, AV in, HDMI in*	
		Antenna	75 Ω Din Type for VHF/UHF in	
	Front	INPUT 4	S-VIDEO, AV in (Audio input is shared with PC INPUT.)	
		PC	Analog RGB in	
MONITOR C	UT Terminal (F	Rear)	AV out	
PHONES OU	JTPUT Termina	al (Front)	16 - 32 Ω recommended	
SUB WOOFER OUTPUT Terminal (Rear)		erminal (Rear)	Variable	
Power Requirement			110 - 240 V(PDP-R06G), 220 - 240V(PDP-R06C), 50/60 Hz, 16 W (0.4 W Standby)	
Dimensions			420 (W) × 90 (H) × 299 (D) mm	
Weight			3.4 kg	

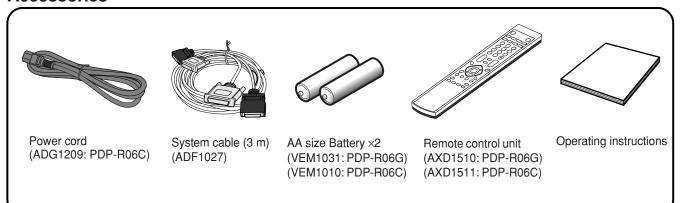
*: This conforms to HDMI1.1 and HDCP1.1.

HDMI (High Definition Multimedia Interface) is a digital interface that handles both video and audio using a single cable. HDCP (High-bandwidth Digital Content Protection) is a technology used to protect copyrighted digital contents that use the Digital Visual Interface (DVI).

• Design and specifications are subject to change without notice.

Accessories

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2. EXPLODED VIEWS AND PARTS LIST

NOTES: • Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List.

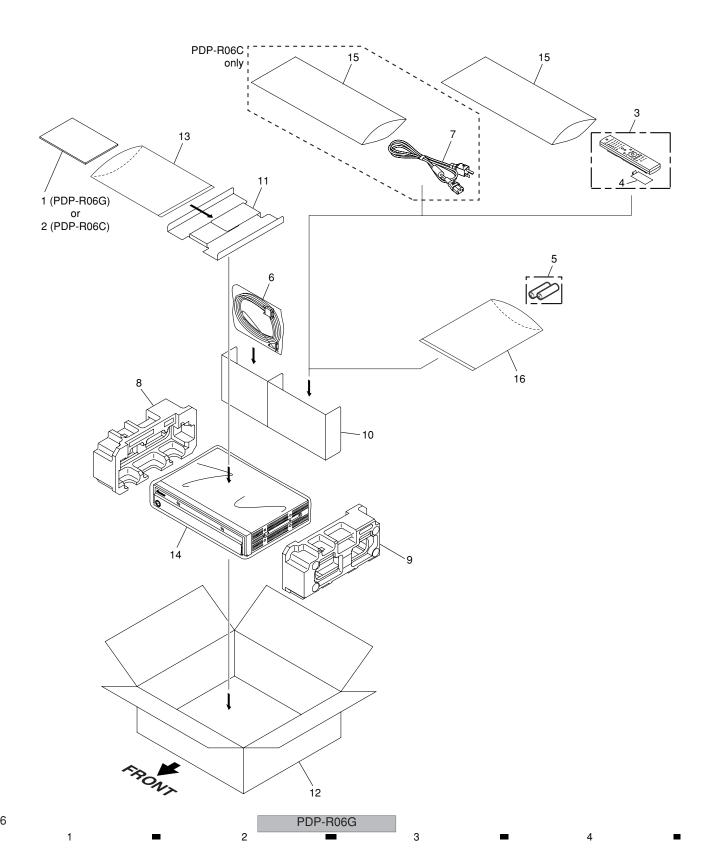
- ullet The \triangle mark found on some component parts indicates the importance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.
- ullet Screws adjacent to lacktriangle mark on product are used for disassembly.
- For the applying amount of lubricants or glue, follow the instructions in this manual. (In the case of no amount instructions, apply as you think it appropriate.)

2.1 PACKING SECTION

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(1) PACKING SECTION PARTS LIST

<u>Description</u>	Part No.
Operating Instructions	See Contrast table (2)
(English, Chinese, Spanish, F	Portuguese)
Operating Instructions	See Contrast table (2)
(Chinese)	
Remote Control Unit	See Contrast table (2)
Battery Cover	AZA7424
Dry Cell Battery (R6P, AA)	See Contrast table (2)
System Cable (3m)	ADF1027
Power Cord	See Contrast table (2)
Pad L	See Contrast table (2)
Pad R	See Contrast table (2)
Accessory Carton	See Contrast table (2)
Manual Case	See Contrast table (2)
Carton	See Contrast table (2)
Catalogue Bag	See Contrast table (2)
Laminate Sheet	AHG1350
	See Contrast table (2)
. •	See Contrast table (2)
	- ()
	Operating Instructions (English, Chinese, Spanish, Formula Instructions (Chinese) Remote Control Unit Battery Cover Dry Cell Battery (R6P, AA) System Cable (3m) Power Cord Pad L Pad R Accessory Carton Manual Case Carton Catalogue Bag

(2) CONTRAST TABLE

PDP-R06G/TLDFXJ and PDP-R06C/WAXU5 are constructed the same except for the following:

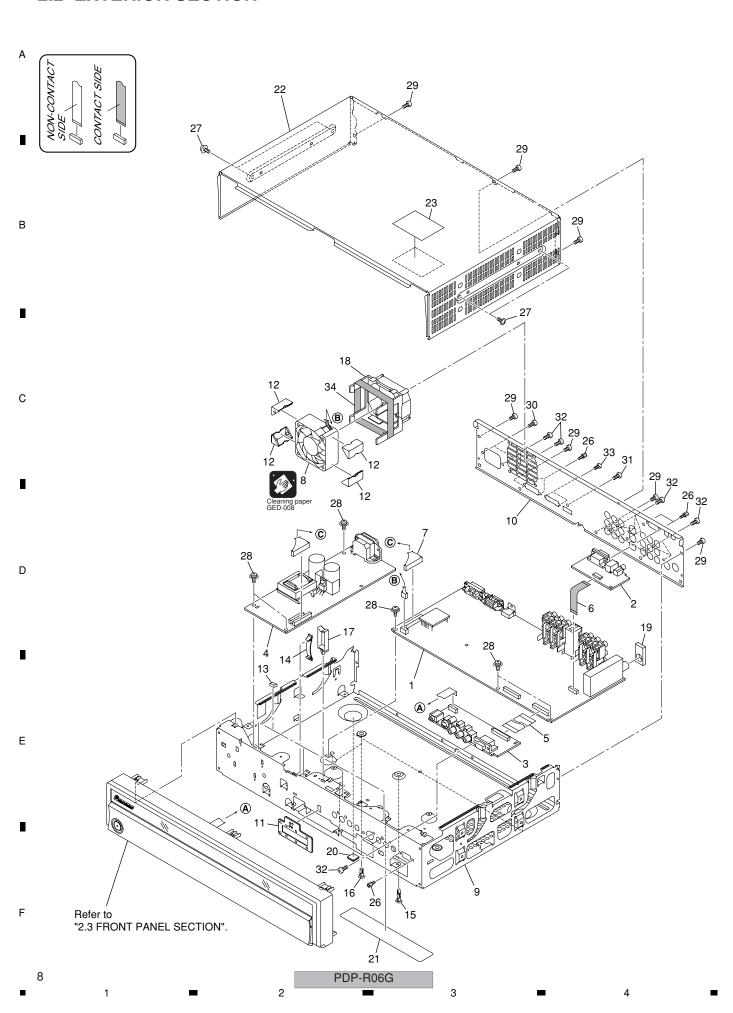
Mark	No.	Symbol and Description	PDP-R06G/TLDFXJ	PDP-R06C/WAXU5
	1	Operating Instructions (English, Chinese, Spanish, Portuguese)	ARE1398	Not used
	2	Operating Instructions (Chinese)	Not used	ARC1545
	3	Remote Control Unit	AXD1510	AXD1511
NSP	5	Dry Cell Battery (R6P, AA)	VEM1031	VEM1010
<u> </u>	7	Power Cord	Not used	ADG1209
	8	Pad L	AHA2447	AHA2449
	9	Pad R	AHA2448	AHA2450
	10	Accessory Carton M	AHD3423	Not used
	10	Accessory Carton C	Not used	AHD3361
	11	Manual Case	AHD3428	AHD3429
	12	Carton G	AHD3449	Not used
	12	Carton C	Not used	AHD3363
NSP	13	Catalogue Bag	AHG1340	AHG1360
	15	Air Cap Bag	AHG1351	AHG1359
NSP	16	Catalogue Bag	AHG1374	AHG1360

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PDP-R06G

2.2 EXTERIOR SECTION



(1) EXTERIOR SECTION PARTS LIST

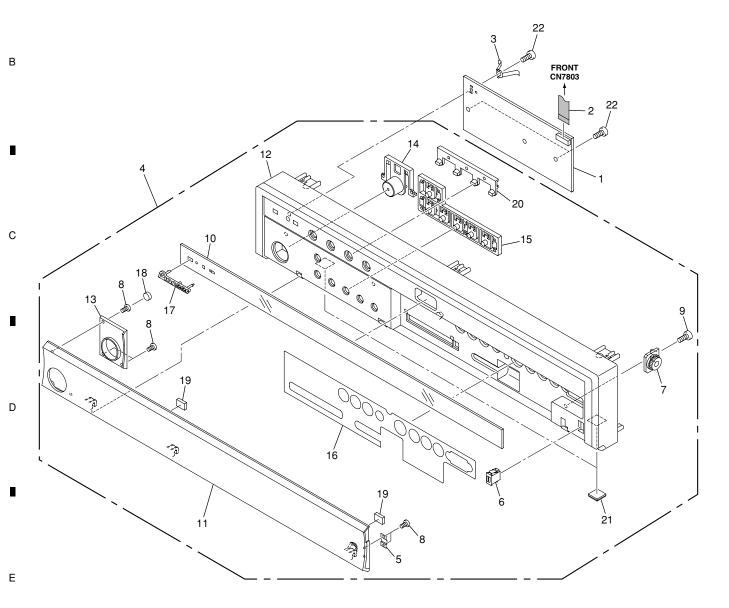
Mark	No.	Description	Part No.	Mark No.	<u>Description</u>	Part No.	
	1	MR MAIN Assy	See Contrast table (2)	21	Bottom Cover	See Contrast table (2)	
	2	SR Assy	AWW1037	22	Metal Bonnet	See Contrast table (2)	Α
	3	FRONT Assy	AWW1051	23	Caution Label (C)	See Contrast table (2)	٨
<u> </u>	4	POWER SUPPLY Unit	AXY1114	24	••••		
	5	Flexible Cable (J201)	See Contrast table (2)	25	••••		
	6	Flexible Cable (J208)	See Contrast table (2)	26	Hex Head Screw	BBA1051	
	7	16P Housing Wire (J101)	See Contrast table (2)	27	Screw	ABZ30P060FTC	
<u> </u>	8	Fan Motor (60 x 25L)	AXM1045	28	Screw	BBB30P080FTC	
	9	Base Chassis	See Contrast table (2)	29	Screw	BBZ30P060FTB	
	10	Terminal Panel	See Contrast table (2)	30	Screw	BBZ30P100FTC	
<u>(1)</u>	11	Shield Plate	ANG2838	31	Screw	BMZ30P060FTC	В
•	12	Floating Rubber 60	AEB1410	32	Screw	BPZ30P080FTB	
	13	Front Panel Spacer	AEB1429	33	Screw	PMZ26P060FTB	
	14	Flat Clamp	AEC1858	34	TERAOKA No.570F 16mm(W)	GYH1001	
	15	Circuit Board Spacer	AEC1969				
	16	Circuit Board Spacer	AEC2028				
	17	Re-used Wire Saddle	AEC2038				
	18	Fan Holder 60	AMR3451				
<u> </u>	19	Gasket M	ANK1774				
	20	Rubber Foot	VEB1349				
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(2) CONTRAST TABLE
PDP-R06G/TLDFXJ and PDP-R06C/WAXU5 are constructed the same except for the following:

Mark	No.	Symbol and Description	PDP-R06G/TLDFXJ	PDP-R06C/WAXU5
	1	MR MAIN Assy	AWV2232	AWV2234
	5	Flexible Cable (J201)	ADD1311	ADD1318
	6	Flexible Cable (J208)	ADD1328	ADD1320
	7	16P Housing Wire (J101)	ADX3140	ADX3147
	9	Base Chassis	ANA1872	Not used
	9	Base Chassis (J)	Not used	ANA1891
	10	Terminal Panel G	ANC2379	Not used
	10	Terminal Panel C	Not used	ANC2380
	21	Bottom Cover (G)	AAX3221	Not used
	21	Bottom Cover (C)	Not used	AAX3222
	22	Metal Bonnet	ANE1653	ANE1644
	23	Caution Label (C)	Not used	AAX3220

2.3 FRONT PANEL SECTION

NOW-CONTACT
SIDE
CONTACT SIDE



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(1) FRONT PANEL SECTION PARTS LIST

Mark No.	<u>Description</u>	Part No.	
1	LED Assy	AWW1043	
2	Flexible Cable (J207)	See Contrast table (2)	Α
<u>^</u> 3	Earth Metal	BNG1336	^
4	Front Panel Assy	See Contrast table (2)	
5	Magnet Catcher	ANG2820	
6	Magnet Holder Assy	AEC1077	
7	Gear Damper	AXA1019	
8	Screw (2 x 3.5)	ABA1329	
9	Screw	BPZ30P080FTB	
10	Indicator Panel	See Contrast table (2)	
11	Door (GC)	AAN1486	В
12	Front Panel	AMB2863	
13	Escutcheon Ring	AAD4134	
NSP 14	Power Button	AAD4135	
NSP 15	Operation Button	AAD4136	
16	Sealing Sheet (G)	AAL2669	
17	Pioneer Name Plate	AAM1107	
18	Door Cushion	AEB1412	
19	Door Cushion S	AEB1425	
NSP 20	LED Lens	AMR3452	_
21	Rubber Foot	VEB1349	С
22	Screw	BPZ30P080FTB	

(2) CONTRAST TABLE PDP-R06G/TLDFXJ and PDP-R06C/WAXU5 are constructed the same except for the following:

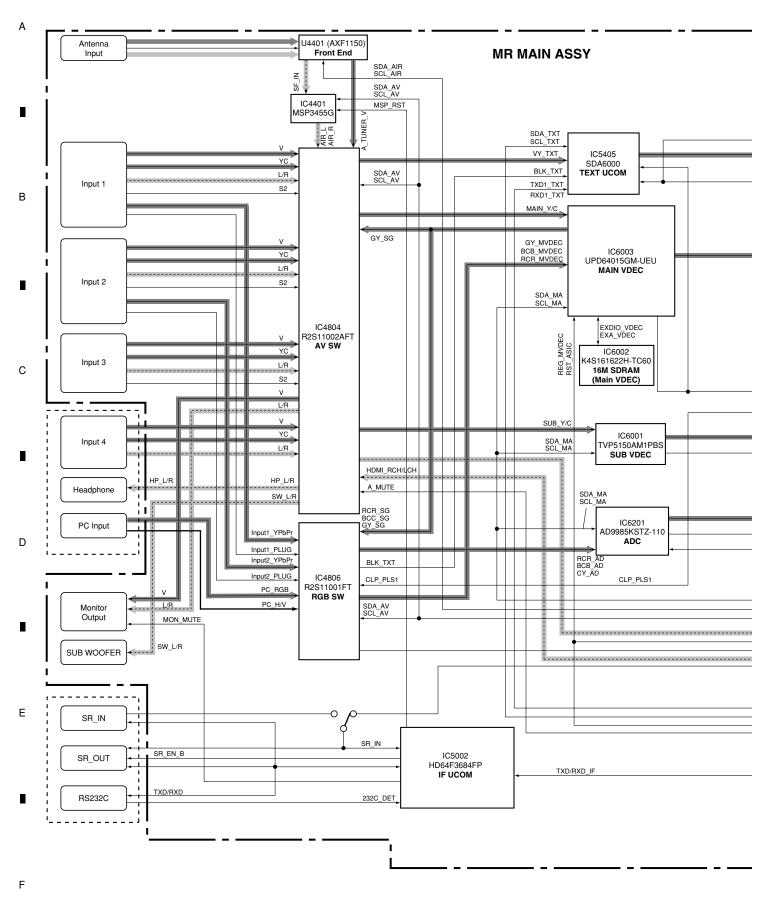
Mark	No.	Symbol and Description	PDP-R06G/TLDFXJ	PDP-R06C/WAXU5
	2	Flexible Cable (J207)	ADD1314	ADD1319
	4	Front Panel Assy G	AXG1032	Not used
	4	Front Panel Assy C	Not used	AXG1033
	10	Indicator Panel (G)	AAK2845	Not used
	10	Indicator Panel (C)	Not used	AAK2846

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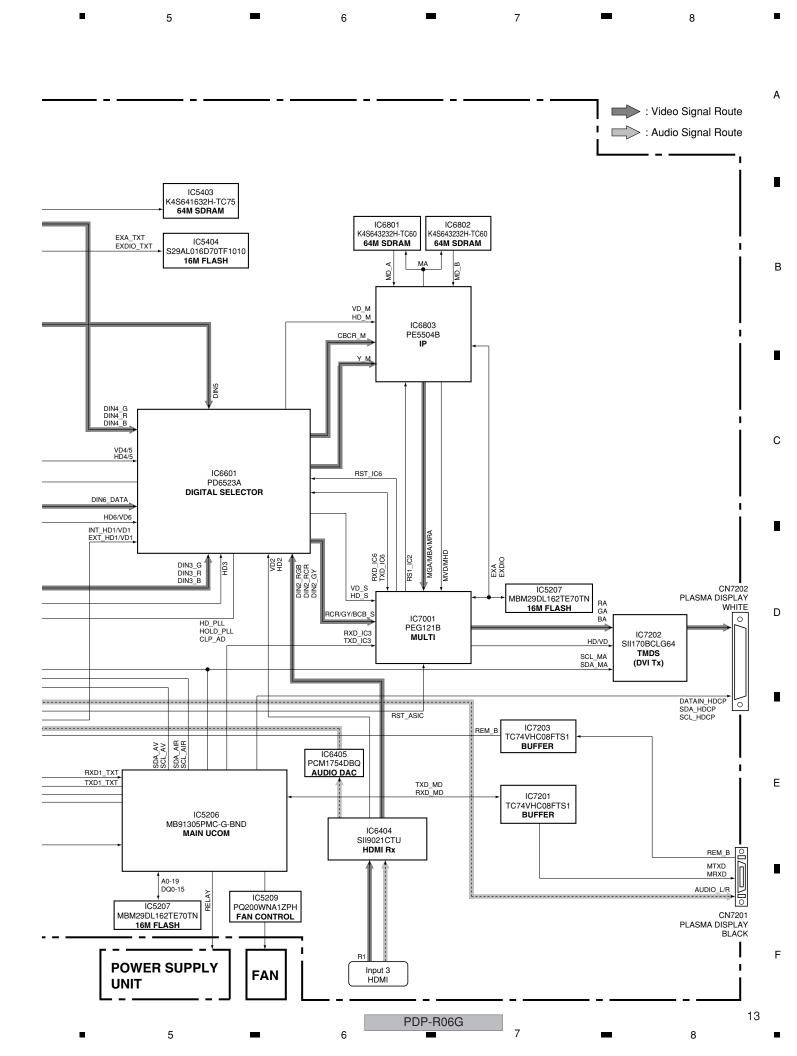
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3.1 OVERALL BLOCK DIAGRAM



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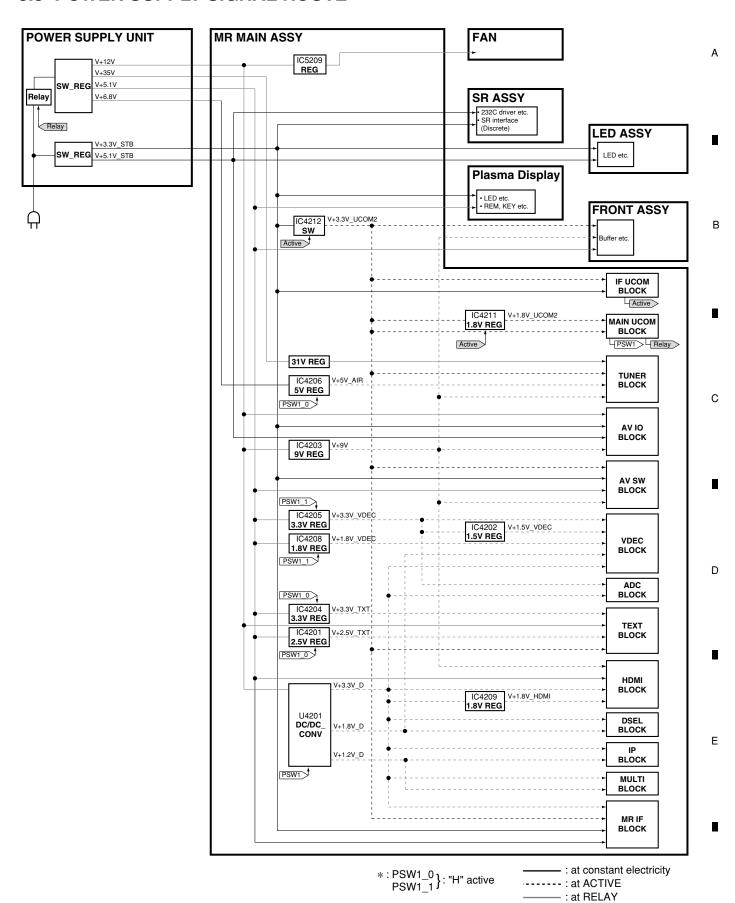
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POWER SUPPLY UNIT

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PDP-R06G

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-----: at PSW1

: Control port "H" active : Control port "L" active

3.4 VOLTAGES

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CN	7004 (AKM1236)	Voltage	CN4001 (AKM12	36)
No.	Name	(V)	Name	No.
1	V+3_3V_STB	3.4	V+3_3V_STB	50
2	LED_ON	0	LED_ON	49
3	LED_OFF	3.4	LED_OFF	48
4	GND[LED MDM]	0	GND[LED MDM]	47
5	V+5 1V STB	5.1	V+5 1V STB	46
6	GND[LED_FCT]	0	GND[LED_FCT]	45
7	KEY_AD1	3.4	KEY_AD1	44
8	KEY AD2	3.4	KEY AD2	43
9	GND	0	GND	42
10	GND[LED_HDD1]	0	GND[LED_HDD1]	41
11	GND[LED_HDD2]	0	GND[LED_HDD2]	40
12	GND[LED_HDD3]	0	GND[LED_HDD3]	39
13	PC_V	0	PC V	38
14	GND	0	GND	37
15	PC_H	0	PC_H	36
16	GND	0	GND	35
17	PC G	2.5	PC G	34
18	GND	0	GND	33
19	PC_B	2.5	PC_B	32
20	GND	0	GND	31
21	PC_R	2.5	PC_R	30
22	GND	0	GND	29
23	GND	0	GND	28
24	HP_PLUG	0	HP_PLUG	27
25	NC	0	NC	26
26	GND	0	GND	25
27	GND	0	GND	24
28	HP_R	2.1	HP_R	23
29	GND	0	GND	22
30	GND	0	GND	21
31	HP_L	2.1	HP_L	20
32	GND	0	GND	19
33	GND	0	GND	18
34	INPUT4_Y	2.5	INPUT4_Y	17
35	GND	0	GND	16
36	INPUT4_C	2.2	INPUT4_C	15
37	GND	0	GND	14
38	INPUT4_SPLUG	5.0	INPUT4_SPLUG	13
39	INPUT4_S2	0	INPUT4_S2	12
40	GND	0	GND	11
41	INPUT4_V	2.5	INPUT4_V	10
42	GND	0	GND	9
43	INPUT4_L	4.5	INPUT4_L	8
44	GND	0	GND	7
45	INPUT4_R	4.5	INPUT4_R	6
46	GND	0	GND	5
47	WE_RDM	0	WE_RDM	4
48	V+3_3V_UCOM	3.4	V+3_3V_UCOM	3
49	V+5V_A	5.1	V+5V_A	2
50	V+9V_A	9.0	V+9V_A	1

CN	CN7804 (AKM1238) Voltage		CN8001 (CKS3826)	
No.	Name	(V)	Name	No.
1	GND	0	GND	12
2	GND	0	GND	11
3	GND	0	GND	10
4	GND	0	GND	9
5	KEY_AD2	3.4	KEY_AD2	8
6	KEY_AD1	3.4	KEY_AD1	7
7	LED_TREC	0	LED_TREC	6
8	V+5_V_STB	5.1	V+5_V_STB	5
9	GND	0	GND	4
10	LED_OFF	3.4	LED_OFF	3
11	LED_ON	0	LED_ON	2
12	V+3 3V STB	3.4	V+3 3V STB	1

ŀ	155 Y					MR MAIN A	ISSY
826)		CN	7601 (CKS3826)	Voltage CN4008 (AKM123		33)	
	No.		No.	Name	(V)	Name	No.
	12		12	V+5_1_STB	5.1	V+5_1_STB	1
	11		11	V+3_3_STB	3.4	V+3_3_STB	2
	10		10	TXD	3.4	TXD	З
	9		9	RXD	3.4	RXD	4
	8		8	232C_DET	0	232C_DET	5
	7		7	SR_EN_B	3.4	SR_EN_B	6
	6		6	GND	0	GND	7
	5		5	REM_B	3.4	REM_B	8
	4		4	SR_IN	3.4	SR_IN	9
	3		3	GND	0	GND	10
	2	l	2	NC	-	NC	11
	1	l	1	GND	0	GND	12

SR ASSY

CN1	01 (B16B-DH-K-S)	Voltogo	CN4006 (KM200N	Δ16)
No.	Name	(V)	Name	No.
16	V+35V	35.8	V+35V	16
15	GND	0	GND	15
14	V+17V	0	V+17V	14
13	GND	0	GND	13
12	V+12V	12.2	V+12V	12
11	GND	0	GND	11
10	V+6_8V	6.6	V+6_8V	10
9	GND	0	GND	9
8	V+5_1V	5.1	V+5_1V	8
7	V+5_1V	5.1	V+5_1V	7
9	V+5_1V_STB	5.1	V+5_1V_STB	6
5	GND	0	GND	5
4	V+3_3V_STB	3.4	V+3_3V_STB	4
ω	GND	0	GND	3
2	RELAY	3.4	RELAY	2
1	AC_DET	3.4	AC DET	1

MR MAIN ASSY

AN MR MAIN ASSY					
		Voltage	CN4009 (AKM12	74)	
No.	Name	(V)	Name No.		

/oltage	CN4009 (AKW12	(74)	
(V)	Name	No.	
6.5	FAN_VCC	1	
0	FAN_NG1	2	ı
0	GND	3	l

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- The \triangle mark found on some component parts indicates the importance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.
- When ordering resistors, first convert resistance values into code form as shown in the following examples.
 Ex.1 When there are 2 effective digits (any digit apart from 0), such as 560 ohm and 47k ohm (tolerance is shown by J=5%, and K=10%).

Ex.2 When there are 3 effective digits (such as in high precision metal film resistors).

 $5.62k \Omega \rightarrow 562 \times 10^{1} \rightarrow 5621 \dots RN1/4PC \boxed{5} \boxed{6} \boxed{2} \boxed{1} F$

■ LIST OF HOLE PCB ASSEMBLIES

Mark	Symbol and Description	PDP-R06G/TLDFXJ	PDP-R06C/WAXU5
	1MR MAIN ASSY	AWV2232	AWV2234
NSP	1MR FUKUGO ASSY 2SR ASSY 2FRONT ASSY 2LED ASSY	AWV2233 AWW1037 AWW1051 AWW1043	AWV2233 AWW1037 AWW1051 AWW1043
<u> </u>	1POWER SUPPLY UNIT	AXY1114	AXY1114

MR MAIN ASSY

AWV2232 and AWV2234 are constructed the same except for the following:

Mark	Symbol and Description	AWV2232	AWV2234
	[MAIN UCOM BLOCK]		
	R5243	RS1/16SS103J	Not used
	R5251	Not used	RS1/16SS103J
	R5252	Not used	RS1/10S0R0J
	R5253	RS1/10S0R0J	Not used

■ PCB PARTS LIST FOR PDP-R06G/TLDFXJ UNLESS OTHER WISE NOTED

Mark No. Descr	ription Part No.	Mark No. Description	Part No.
MR MAIN ASSY		[REG BLOCK]	
[BOARD IF BLOCK]		SEMICONDUCTORS	
SEMICONDUCTORS		IC4212	BD6522F
Q4003.Q4004	2SA1586	IC4208,IC4211	MM1661JH
Q4001	DTA124EUA	IC4202 IC4209	NCP1117ST15 NCP1117ST18
Q4002	TPC6104	IC4209 IC4201	PQ025ENA1ZPH
D4001-D4003,D4005	1SS355	104201	FQ023ENATZFH
0.4.04.017.0.00		IC4204,IC4205	PQ033ENA1ZPH
CAPACITORS		IC4206	PQ050DNA1ZPH
C4002	CKSRYB105K10	IC4203	PQ090DNA1ZPH
C4003,C4004	CKSSYB104K10	Q4201	DTC124EUA
DECISTORS		D4201-D4206,D4208,D4209,D4211	1SS355
RESISTORS R4021-R4023	RS1/10S0R0J	OOU O AND EU TERO	
R4021-R4023 R4007	RS2LMF8R2J	COILS AND FILTERS	
Other Resistors	RS1/16S###J	L4201 INDUCTOR	BTH1111
Other resistors	1131/103###3		BTX1042
OTHERS		EMI FILTER	CCG1162
CN4008 12P FFC CONN	IECTOR AKM1233		
CN4001 50P CONNECTO	OR AKM1236	CAPACITORS	
CN4009 CONNECTOR 3	BP AKM1274	C4201,C4206,C4209 (10/6.3V)	ACG7046
CN4006 16P CONNECTO	OR KM200NA16	C4215,C4220,C4233 (10/6.3V)	ACG7046
		C4235,C4240 (10/6.3V)	ACG7046
		C4250,C4253 (10/6.3V)	ACG7046
		C4257,C4260,C4263 (10/6.3V)	ACG7046

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	No Description	Dort No.	Mark No December	Dout No
	No. Description		Mark No. Description	Part No.
	213 (100/16V)	ACH1394	C4402,C4405,C4406,C4425,C4426	CKSSYF104Z16
	210,C4244,C4269 273	ACH1429 CCSSCH101J50	C4434,C4435,C4447,C4451,C4460 C4465	CKSSYF104Z16 CKSSYF104Z16
_	273 205,C4216,C4219,C4221,C42		C4465 C4414,C4437,C4445	DCH1165
	224,C4228,C4238,C4264	CEHVKW101M6R3	04414,04407,04440	DOITITIOS
· · ·	,, -, -, -, -, -, -, -, -, -, -, -,		<u>RESISTORS</u>	
	226	CEHVKW220M16	All Resistors	RS1/16S###J
_	214	CKSRYB104K16		
	203,C4217,C4223	CKSRYB105K10	<u>OTHERS</u>	
	229,C4237,C4252 232,C4234	CKSSYB104K10 CKSSYB471K50	X4401 CRYSTAL (18.432MHz)	ASS1196
C4.	232,04234	CK551B471K50		AXF1150
C4:	204,C4212,C4227,C4236,C42	51 CKSSYF104Z16		
C4:	261,C4262	CKSSYF104Z16	[AV IO BLOCK]	
C4:	211,C4225,C4256	DCH1165	SEMICONDUCTORS	
			Q4614,Q4626,Q4639	2SA1586
	<u>ISTORS</u>		Q4618,Q4622-Q4624,Q4637	2SC4116
	221,R4226	RS1/10S0R0J	Q4619,Q4620,Q4640	2SD2114K
Otr	ner Resistors	RS1/16S###J	Q4616,Q4621,Q4627	DTA124EUA
ОТН	FRS		Q4617,Q4628	DTC124EUA
	ERS 201 DD CONTROL UNIT	AXY1117	0.400	
04,	EGT DD GOINTHOL DINH	DATITI	Q4625,Q4638	HN1A01FU
			D4611,D4612,D4615	1SS301
[TUN	IER BLOCK]		D4606	1SS355
_	ICONDUCTORS		COILS AND FILTERS	
	401	MSP3455G	L4601-L4606	LCTAW1R0J25
Q4	404	2SA1586	L4607,L4608	LCTAW560J25
	401,Q4402	2SC4116	,	
	414	DTA124EUA	CAPACITORS	
Q4	413,Q4415	DTC124EUA	C4621 (10/6.3V)	ACG7046
04	407.Q4408	HN1A01ELL	C4657 (100/16V)	ACH1394
	407,Q4408 405	HN1A01FU HN1B04FU	C4623	ACH1419
	409	HN1C01FU	C4658	CCG1205
D4		UDZS33(B)	C4643	CKSRYB104K1
	403	UDZS8R2(B)	C4603,C4608,C4613,C4631-C4639	CKSRYB105K1
			C4641.C4642.C4645-C4648	CKSRYB105K1
	<u>LS AND FILTERS</u>		C4644	CKSRYB224K1
	101-L4403 CHIP COIL	BTH1119	⚠ C4651-C4656	CKSSYB102K5
	105,L4406	LCTAW150J2520	C4601,C4604-C4606,C4609-C4611	CKSSYB103K1
L44	10 <i>7</i> 104	LCTAW4R7J2520 LCTAW8R2J2520	04044 04045 04040 0405	01/00/10 : 551 :
	104 101,F4402 FERRITE BEAD	VTF1080	C4614,C4615,C4649,C4650	CKSSYB103K1
1 44	IVI,I TTOL I LITTILL DEMO	V 11 1000	C4602,C4607,C4612 C4622	CKSSYB473K1 CKSSYF104Z1
CAP	ACITORS		C4622,C4624,C4625,C4628	DCH1165
	404,C4407,C4410 (10/6.3V)	ACG7046	J.020,04024,04020,04020	20111100
	415,C4416 (10/6.3V)	ACG7046	RESISTORS	
	429,C4459 (10/6.3V)	ACG7046	R4670	RS1/10S121J
	424 (3.3/50V)	ACH1385	R4658	RS1/10S151J
C4	449	CCSRCH680J50	R4602,R4603,R4605,R4606	RS1/16S75R0F
0.4	440	00000 1000000	R4608-R4612,R4619-R4622	RS1/16S75R0F
_	442 417,C4418	CCSRCJ3R0C50 CCSSCH100D50	R4625,R4626	RS1/16S75R0F
	417,04418 450	CCSSCH100D50 CCSSCH121J50	Other Peciators	DC1/160###1
	456	CCSSCH121330	Other Resistors	RS1/16S###J
_	448	CCSSCH470J50	OTHERS	
			JA4603 9P PIN JACK	AKB1319
	428,C4443	CCSSCH560J50	JA4602 9P PIN JACK	AKB1319 AKB1330
	441	CCSSCH5R0D50	JA4601 4P MINI DIN (S)	AKP1280
	409,C4423	CEHVKW100M16	JA4604 1P JACK	VKB1159
_	422 420	CEHVKW470M16 CKSRYB332K50		
04		ONOTH BOOLINGO	IAM SWIPL OOK!	
	401,C4413	CKSRYF104Z50	[AV SW BLOCK]	
	403,C4421,C4427,C4430,C44		<u>SEMICONDUCTORS</u>	DUGGAGE
	444,C4455,C4461	CKSSYB102K50	IC4807	BH3544F
C4	408,C4439,C4446	CKSSYB103K16	IC4805 IC4806	NJM12904V R2S11001FT
	438,C4454	CKSSYB472K25	IC4806 IC4804	R2S11001F1
			104004	DZOLIUUZAET
			IC4809	TC7WH123FU
		PDP-I	IC4809	

<u>ark No.</u>	Description	Part No.	Mark No. Descri	ption Part No.
	1809,Q4818,Q4820	2SA1586	[MAIN UCOM BLOCK]	
Q4808,Q4811-Q4	4813,Q4821	2SC4116	SEMICONDUCTORS	
Q4814		DTA124EUA	IC5202	BR24L64F-W
Q4815		DTC124EUA	IC5206	MB91305PMC-G-BND
D4802,D4806		1SS301	IC5207	MBM29DL162TE70TN
			IC5210	MM1522XU
APACITORS			IC5209	PQ200WNA1ZPH
C4916 (4.7/10V)		ACG1122	.00200	. 4200
C4821,C4835,C4	871 (10/6.3V)	ACG7046	IC5203	PST3628UR
C4875,C4896,C4	923 (10/6.3V)	ACG7046	IC5201	TC74VHC125FTS1
C4877,C4880		CCSRCH181J50	Q5202	2SJ461A
C4859		CCSRCH331J50	Q5204	DTC124EUA
			Q5201	SM6K2
C4861		CCSRCH680J50		
C4885,C4888		CCSRCH681J50	D5203	1SS355
C4822,C4862		CEHVKW101M6R3	D5201	SML-311UT
C4898		CEHVKW470M6R3		
C4802,C4805,C4	806,C4808	CKSRYB105K10	CAPACITORS	
			C5235	CCSRCH221J50
, ,	820,C4833,C4834	CKSRYB105K10	C5244.C5245	CCSSCH120J50
,	841,C4847,C4848	CKSRYB105K10	C5217,C5218,C5237,C523	
, ,-	878,C4879,C4889	CKSRYB105K10	C5246-C5249	CCSSCH470J50
C4894,C4895,C4	922	CKSRYB105K10	C5238	CEHVKW100M35
C4853-C4858,C4	860,C4865	CKSSYB103K16	00200	OEI IVITAN IOOIVIOO
			C5201	CEHVKW101M6R3
C4869,C4870,C4	890-C4893	CKSSYB103K16	C5261-C5263	CKSSYB102K50
C4807,C4809		CKSSYB104K10	C5216,C5233	CKSSYB102K30
	845,C4846,C4864	CKSSYF104Z16	C5215	CKSSYB472K25
C4867,C4868,C4	873,C4874	CKSSYF104Z16	C5253	CKSSYF103Z50
C4881-C4884,C4	886,C4887	CKSSYF104Z16	00200	51.00 11 100£00
			C5202-C5209,C5211-C521	4,C5219 CKSSYF104Z16
	917,C4921,C4925	CKSSYF104Z16	C5222-C5232,C5234,C525	
C4844,C4863,C4	866,C4872,C4876	DCH1165	C5236	DCH1165
ESISTORS			RESISTORS	
R4975,R4999		RD1/2LMF120J	R5262,R5268	ACN1248
R4784,R4786		RS1/16S1800F	R5205,R5206 R5205,R5213	RAB4CQ101J
R4785,R4787		RS1/16S5600F	R5205,R5213 R5253	RS1/10S0R0J
Other Resistors		RS1/16S###J	R5253 R5283	RS1/10S0R0J RS1/16S1001F
			R5283	RS1/16S1001F RS1/16S4701F
			NJ202	NO1/1004/UTF
UCOM BLO	CK]		R5273	RS1/16S8201F
MICONDUC			Other Resistors	RS1/16S8201F RS1/16S###J
IC5002	. 31.0	HD64F3684FP	Other Desistors	1101/100###J
IC5002 IC5003		PST9230N	OTHERS	
IC5003 IC5001		TC74VHC08FTS1	· · · · · · · · · · · · · · · · · · ·	D AVM4.004
IC5001 IC5004		TC7W126FU	CN5202 50P CONNECTO	-
Q5004		DTC124EUA	K5201,K5202 TEST PIN	AKX9002
×∪∪∪ I		DIOIZALUA	X5201 CERAMIC RESONA	ATOR ASS1178
PACITORS				
		000001400150	ITEVT HOOM DI ACCO	
C5007,C5008		CCSSCH180J50	[TEXT UCOM BLOCK]	
C5001		CEHVKW101M6R3	<u>SEMICONDUCTORS</u>	
C5010	000 CE040	CKSSYB472K25	IC5403	K4S641632H-TC75
C5002-C5005,C5	0009,05012	CKSSYF104Z16	IC5404	S29AL016D70TFI010
CIOTODO			IC5405	SDA6000
SISTORS			IC5407	TC74LCX125FT
R5002,R5007,R5	025-R5027	RAB4CQ103J	IC5402	TC7SH04FUS1
Other Resistors		RS1/16S###J		
			IC5406	TC7W126FU
HERS			Q5401,Q5406	DTA124EUA
X5002 CERAMIC		ASS1168	Q5403,Q5407	DTC124EUA
X5001 CRYSTAL	OSCILLATOR	ASS1172	D5404	1SS355
			D5401	UDZS12(B)
			D5402	UDZS3R0(B)
			D5403	UDZS3R9(B)
			COILS AND FILTERS	
			COILS AND FILTERS ⚠ F5402,F5403 EMI FILTER	CCG1162

PDP-R06G

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	Mark No. Description	Part No.	Mark No.	Description	Part No.
	CAPACITORS		[ADC BLOCK]		
	C5412,C5438,C5453 (10/6.3V) C5422,C5423	ACG7046 CCSSCH200J50	SEMICONDU IC6201	<u>CTORS</u>	AD9985KSTZ-110
Α	C5404 C5403	CKSSYB102K50 CKSSYB103K16	COILS AND F	III TEDO	
	C5445	CKSSYB104K10	<u>COILS AND F</u> <u>↑</u> F6201,F6204 E		CCG1162
	C5405,C5406,C5408,C5410,C5413 C5416,C5418,C5420,C5425,C5427	CKSSYF104Z16 CKSSYF104Z16	CAPACITORS	<u>)</u>	
	C5429-C5431,C5434,C5435,C5440	CKSSYF104Z16	C6205,C6209	20040	CKSSYB104K10
	C5442,C5446,C5449,C5451,C5454 C5456,C5458,C5460,C5476	CKSSYF104Z16 CKSSYF104Z16	C6207,C6210,C C6202	J0218	CKSSYB473K16 CKSSYB822K16
	C3436,C3436,C3460,C3476	CK331F104Z10	C6201	2000 0000	CKSSYB823K10
	RESISTORS	AON4054	C6203,C6204,C	J6206,C6206	CKSSYF104Z16
_	R5409 R5404,R5428,R5429,R5434,R5435	ACN1251 BCN1067	C6211,C6212,C	C6215-C6217	CKSSYF104Z16 CKSSYF104Z16
В	R5439,R5457,R5476	RAB4CQ103J	C6222-C6224		CK551F104Z16
	R5432,R5460 Other Resistors	RAB4CQ680J RS1/16S###J	RESISTORS		
			R6213,R6218,F R6202	R6223	BCN1067 RS1/16SS2701F
	OTHERS X5401 CRYSTAL	ASS1193	Other Resistors		RS1/16S###J
	NOTO TOTAL	7.001100			
	[VDEC BLOCK]		[HDMI BLOCK		
	SEMICONDUCTORS		SEMICONDU IC6403	<u>CTORS</u>	BR24L02FJ-W
	IC6002	K4S161622H-TC60	IC6403		PCM1754DBQ
С	IC6001 IC6003	TVP5150AM1PBS UPD64015GM-UEU	IC6404		SII9021CTU
	Q6002	DTA124EUA	Q6416 Q6414		2SA1586 DTA124EUA
	COILS AND FILTERS		Q6415		DTC124EUA
	⚠ F6001,F6002,F6008-F6011,F6022	CCG1162	Q6415 Q6405		HN1K02FU
	EMI FILTER		Q6404		RN1902
	CAPACITORS		D6408 D6407		1SS301 UDZS6R8(B)
	C6056,C6088 (10/6.3V)	ACG7046			()
	C6059,C6060 C6078,C6083	CCSSCH100D50 CCSSCH8R0D50	COILS AND F		CCG1162
	C6048-C6050	CKSRYB105K10	EN 0401 LIVITIL	II LN	CCGTT02
D	C6062,C6069	CKSSYB103K16	CAPACITORS	-	
	C6046,C6051,C6052,C6054,C6058	CKSSYB104K10	C6491 (10/6.3V	() C6405,C6407,C6411	ACG7046 CCSSCH101J50
	C6063,C6064,C6066,C6067 C6072,C6073,C6075-C6077	CKSSYB104K10 CKSSYB104K10	C6419,C6426,C	C6428,C6430,C6432	CCSSCH101J50
	C6081,C6082,C6084,C6085	CKSSYB104K10		C6438,C6440,C6442 C6448,C6449,C6454	CCSSCH101J50 CCSSCH101J50
	C6001-C6008,C6012-C6028	CKSSYF104Z16	, ,	, ,	
	C6031-C6045,C6047,C6053,C6055	CKSSYF104Z16		C6464,C6466,C6468 C6474,C6476,C6478	CCSSCH101J50 CCSSCH101J50
	C6061,C6065,C6068,C6070,C6071 C6074,C6079,C6080,C6090,C6091	CKSSYF104Z16 CKSSYF104Z16	C6480,C6482	70 17 1,00 17 0,00 17 0	CCSSCH101J50
	00074,00079,00080,00090,00091	CN3311 104210	C6462,C6463 C6484		CCSSCH120J50 CEHVKW220M6R3
_	RESISTORS	4014040			
Ε	R6010,R6062,R6068,R6072 R6065,R6073	ACN1246 BCN1067		C6406,C6408,C6410 C6416,C6418,C6420	CKSSYF104Z16 CKSSYF104Z16
	R6007,R6023,R6030,R6071	RAB4CQ220J	, ,	C6427,C6429,C6431	CKSSYF104Z16
	R6063 R6038,R6039,R6049	RS1/16SS1001D RS1/16SS2000F	, ,	C6437,C6439,C6441	CKSSYF104Z16
			C6443,C6445,C	C6447,C6450,C6451	CKSSYF104Z16
	R6054 R6052	RS1/16SS2201D RS1/16SS6200D		C6458,C6460,C6461 C6469,C6471,C6473	CKSSYF104Z16 CKSSYF104Z16
	Other Resistors	RS1/16S###J	, ,	C6479,C6481,C6483	CKSSYF104Z16
	OTHERS		C6490		CKSSYF104Z16
	X6001 CRYSTAL	ASS1189	RESISTORS		
F	X6002 CRYSTAL	ASS1191	R6418,R6419,F	R6421	ACN1251
			R6414 R6465		RAB4CQ100J RAB4CQ103J
			R6438		RAB4CQ470J
	20		R6416		RAB4CQ680J
	20 1 ■	PDP-R060	3	_	4
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Mark No. Description	Part No.	Mark No. Description	Part No.	
Other Resistors	RS1/16S###J	[MULTI BLOCK]		
		SEMICONDUCTORS		
<u>OTHERS</u>		IC7002	MBM29DL162TE70TN	
JA6402 HDMI CONNECTOR	AKP1278	IC7001	PEG121B	
X6401 CRYSTAL	ASS1192	IC7004	TC74VHC08FTS1	
		0011 0 4115 511 7550		
DSEL BLOCK]		COILS AND FILTERS	0004400	
EMICONDUCTORS		♠ F7001-F7005 EMI FILTER	CCG1162	
IC6601	PD6523A	CAPACITORS		ĺ
IC6602	TC74LCX125FT	C7052	CKSSYB102K50	ļ
		C7006,C7008,C7010-C7017,C7019	CKSSYF104Z16	
COILS AND FILTERS		C7021,C7023,C7024,C7026-C7029	CKSSYF104Z16	
∱F6604 CHIP BEAD FILTER	ATX1058	C7032-C7034,C7036,C7037	CKSSYF104Z16	
	CCG1162	C7039-C7042,C7044,C7046-C7048	CKSSYF104Z16	
A DA OLTO DO		C7050	CVCCVE104716	
CAPACITORS	1007040	C7050	CKSSYF104Z16	
C6632 (10/6.3V) C6604	ACG7046 CCSRCH221J50	RESISTORS		
C6631	CKSSYB102K50	R7011,R7013,R7024,R7032,R7036	ACN1246	
C6601-C6603,C6607-C6610	CKSSYF104Z16	R7062-R7064	ACN1251	
C6613-C6617,C6619,C6621-C6623	CKSSYF104Z16	R7015,R7023	RAB4CQ101J	
		R7016,R7018,R7070	RAB4CQ103J	
C6625-C6627,C6629,C6630	CKSSYF104Z16	R7060	RAB4CQ680J	
AFCICTORS		Others Desistant	D04/400###	
RESISTORS	AON4054	Other Resistors	RS1/16S###J	
R6603-R6605	ACN1251			
R6611,R6614,R6618 R6613,R6620	BCN1071 RAB4CQ101J	[MR IF BLOCK]		(
Other Resistors	RS1/16S###J	SEMICONDUCTORS		
	. 10 17 100 11 11 10	IC7202	SII170BCLG64	
OTHERS		IC7201,IC7203	TC74VHC08FTS1	
X6601 CRYSTAL	ASS1194	Q7206	2SA1586	
		Q7203,Q7207,Q7210	DTA124EUA	
		Q7211	DTC124EUA	
P BLOCK]		Q7209	HN1C01FU	
SEMICONDUCTORS		Q7209 Q7201	RN1902	
IC6801,IC6802	K4S643232H-TC60	D7202-D7206	1SS355	
IC6803	PE5504B			
OILS AND FILTERS		COILS AND FILTERS		[
L6801-L6804 CHIP BEAD FILTER	BTX1042	♠ F7204-F7207 EMI FILTER	ATF1209	
		⚠ L7201 CHIP BEAD FILTER	BTX1042	
CAPACITORS		⚠ F7201-F7203 EMI FILTER	CCG1162	
C6801 (10/6.3V)	ACG7046	⚠F7208 EMI FILTER	CCG1162	
C6863	CKSSYB102K50	CAPACITORS		
C6802,C6804,C6807-C6809,C6813	CKSSYF104Z16	C7203,C7207,C7208 (10/6.3V)	ACG7046	ļ
C6815-C6817,C6821,C6824-C6828	CKSSYF104Z16	C7226,C7227	CCSSCH100D50	
C6830,C6831,C6834,C6835	CKSSYF104Z16	C7201,C7204,C7211,C7213,C7214	CCSSCH101J50	
C6839-C6862	CKSSYF104Z16	C7216,C7217,C7219,C7221,C7222	CCSSCH101J50	
		C7223	CKSSYB102K50	
RESISTORS		C7200 C7215 C7220 C7205 C7220	CKSSAD424KEV	E
R6833,R6838	ACN1246	C7209,C7215,C7220,C7225,C7228 C7202,C7205,C7206,C7210,C7212	CKSSYB471K50 CKSSYF104Z16	١
R6841,R6844-R6847	ACN1251	C7218.C7224	CKSSYF104Z16	
R6813,R6814,R6816,R6820,R6821	BCN1067	,		
R6823,R6825,R6827,R6828	BCN1067 BCN1071	<u>RESISTORS</u>		
R6818	DON TO / T	R7215	RAB4CQ101J	
R6832	RAB4CQ101J	R7216	RS1/16S5100F	I
R6817	RAB4CQ470J	Other Resistors	RS1/16S###J	
D0017	RS1/16S###J	OTHERS		
Other Resistors		<u>OTHERS</u>		
		CNIZOO1 COCKET (COD)	AKD1006	
		CN7201 SOCKET (20P)	AKP1226 AKP1250	
		CN7201 SOCKET (20P) CN7202 DVI SOCKET (24P)	AKP1226 AKP1250	

5 ■ PDP-R06G 7 ■ 7

2 3 Mark No. **Description** Part No. **Description** Mark No. JA7801 4P MINI DIN SOCKET (S) **SR ASSY 1** 7805 SCREW TERMINAL **SEMICONDUCTORS** IC7601 MAX3232CPW IC7603 TC74VHC00FTS1 IC7602 TC74VHC125FTS1 **LED ASSY** Q7601,Q7605 2SA1586 **SEMICONDUCTORS** 2SC4116 Q7603 Q8004 Q7602,Q7604,Q7606 DTC124EUA Q8002 D7609-D7612 1SS355 D8003 D8004 **CAPACITORS** SWITCHES AND RELAYS C7608, C7611 CEHVKW100M16 C7603-C7607,C7609,C7610 CKSSYF104Z16 S8001-S8006 **CAPACITORS RESISTORS** All Resistors RS1/16S###J C8005, C8006 C8001,C8002 **OTHERS RESISTORS** JA7603 MINI JACK (4P) AKN1073 CN7602 9P D-SUB SOCKET AKP1213 All Resistors CKS3826 CN7601 CONNECTOR JA7602 JACK RKN1004 **OTHERS** CN8001 CONNECTOR **FRONT ASSY POWER SUPPLY UNIT SEMICONDUCTORS** IC7801 BR24C21FJ POWER SUPPLY Unit has no service part. IC7802 TC74VHC08FTS1 Q7806-Q7808 2SC4116 DTC124EUA Q7804,Q7805 1SS301 D7813 D7816-D7818 1SS302 D7801-D7803 UDZS5R1(B) D7809-D7812,D7814,D7815 UDZS5R6(B) D7804,D7808 UDZS9R1(B) **COILS AND FILTERS** L7803,L7804 LCTAW560J2520 **CAPACITORS** C7821, C7827 (10/6.3V) ACG7046 ACG7046 C7829, C7830 (10/6.3V) C7822,C7823 CCSRCH220J50 C7817,C7818 CEHAT471M10 C7803,C7804 CKSRYB103K50 C7805.C7808.C7809.C7813 CKSRYB105K10 C7831, C7832, C7834 CKSRYB105K10 C7801 CKSRYB473K16 **⚠** C7839,C7840 CKSSYB102K50 C7802, C7820, C7824 CKSSYF104Z16 C7819.C7835 DCH1165 **RESISTORS** R7801,R7803,R7809,R7857-R7859 RS1/16S75R0F Other Resistors RS1/16S###J **OTHERS** JA7803 PIN JACK (3P) AKB1303

22

CN7803 12P FFC CONNECTOR

CN7804 50P CONNECTOR

CN7806 15P D-SUB SOCKET

CN7801 MINI JACK

PDP-R06G

AKM1233

AKM1236 AKN1028

AKP1214

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Part No.

AKP1238

VNE1949

DTC124EUA

SML-311UT SML310BA1T

ASG1088

CCSRCH101J50 CKSSYF104Z16

RS1/16S###J

CKS3826

RN2902

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6.1 POSSIBLE CASES WHERE READJUSTMENT IS REQUIRED

6

■ When any of the following assemblies is replaced

POWER SUPPLY Unit

No adjustment required

No adjustment required

No adjustment required

No adjustment required

■ When any part in the following assemblies is replaced

POWER SUPPLY Unit

The assembly must be replaced as a unit, and no part replacement is allowed.

No adjustment required.
However, IC4804, IC4806, IC5207, IC6001, IC6003 and IC6201 must not be replaced. In this case, the assembly must be replaced as a unit.

Other assemblies

No adjustment required

23

6.2 SERVICING USING ONLY THE MEDIA RECEIVER

For servicing of the PDP-436HD and PDP-506HD-series Plasma Display using only the Media Receiver, the following two methods can be used:

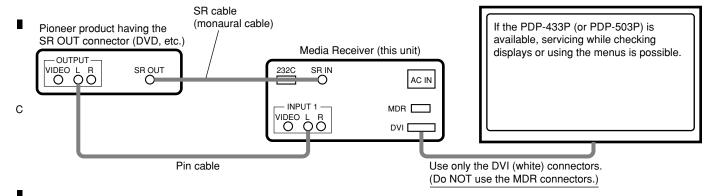
Remote controlling using SR connections

About connections

Connect the SR OUT connector of a Pioneer product having that connector (a DVD in the following example) and the SR IN connector of the

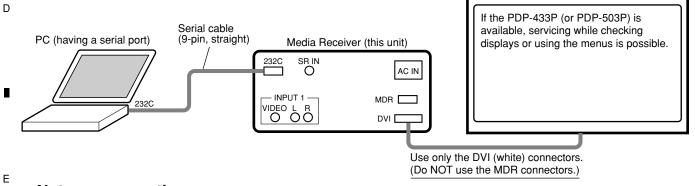
Media Receiver, using the SR cable. As the remote control sensor is not provided with the Media Receiver, this connection is required for using the remote control unit if the panel is not available. In this case, aim the remote control unit at the remote control sensor of the device (DVD in this case).

- Connect either the audio or the video output of the device (DVD in the example) and the corresponding audio or video input of the Media Receiver, using a cable with phono plugs. This connection is required in order to use ground in common with the SR cable, because with the SR cable connection the ground connection for signal reference is not available. In the example, the audio L channel is used, but the audio R channel or video can be used instead.
- If the plasma display for a previous model, such as the PDP-433P or PDP-503P, is available, servicing while checking displays or using the menus is possible. For this, connect only the DVI connectors (white) of the Media Receiver and the plasma display. The MDR connector of the Media Receiver must not be used, even though it has the same shape and number of pins, because signals assigned to the connectors



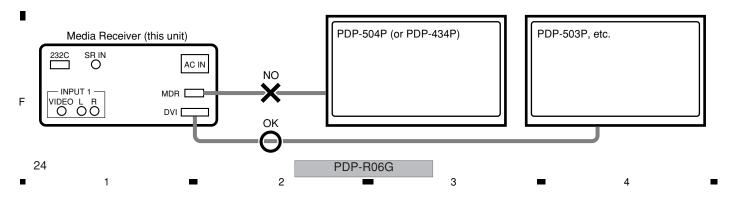
RS-232C control using a PC

RS-232C control is not available in shipment. Please set baud rate of PC in 38400bps. For connection with the PC, use a straight cable.



Note on connection

If the MDR connector of the PDP-436HD or PDP-506HD-series is used, it is considered that the PDP-436P (or PDP-506P) is connected, and the Media Receiver operates on such precondition, **which may result in a failure of the Media Receiver. Be sure not to connect to the MDR connector.** (Do NOT use the MDR connector when servicing the Media Receiver alone.)



To operate in Service Factory mode, use the supplied remote control unit.

■ How to enter Service Factory Mode

While in Standby mode, follow the below procedures with the remote control to enter Service Factoy mode.

- 1. Press the [DISPLAY] key.
- 2. 3 second counter will start.
- 3. After 3 seconds, press [LEFT] key.
 - (If no operation is done within 10 seconds, the Service
- 4. 5 Second counter will start.

- 5. Before 5 second counter ends, press [UP] key.
- 6. Before 5 second counter ends, press [LEFT] key.
- 7. Before 5 second counter ends, press [RIGHT] key.
- 8. Before 5 second counter ends, press [POWER] key.
- Factory routine is cleared, and the standby mode is returned) 9. If the producedure is correct with the given time, the Service Factory mode is up and ready.

Operation in Service Factory mode

• Functions whose settings are set to OFF

The settings for the following functions are set to OFF when Service Factory mode is entered (including when the "FAY" command is received):

- Two-screen operations (input function set on the main side is selected)
- P ZOOM
- FREEZE
- Detection of the TRAP switch (The log in the EEPROM is retained.) (KUC type only)

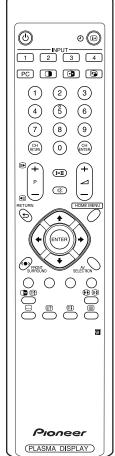
User data

User data will be treated as follows:

- · User data on picture- and audio-quality adjustments are not reflected, and factory-preset data are output (user data will be retained in memory). When the unit enters Factory mode, the current audio-quality adjustment data will still be retained in
- · As to data on various settings, user data will be applied to the items that are associated with signal format change (screen size switching, etc.).
- · Data on screen (i.e., screen position; meaning clock dividers, and not including data on screen size) are reset to the default values (data stored in memory will be retained). Screen size will be retained.

■ Remote control codes in Service Factory mode

SR Function	Main Function	Remarks
Muting	Switching the main items	Shifting to the next main item (top)
DOWN	Switching the subtitled items	Shifting downward to the next subtitled item
UP	Switching the subtitled items	Shifting upward to the next upper layer
LEFT	Increasing the adjustment value	Increasing the adjustment value
RIGHT	Decreasing the adjustment value	Decreasing the adjustment value
SET	Switching layers	Shifting downward or upward to the next lower or upper layer
INPUT	Selecting input	Shifting the input to the next function
INPUTxx	Selecting input	Switching the input to xx
CH+	Increasing the channel number	Advancing a preset channel (effective when Function is set to TV)
CH-	Decreasing the channel number	Turning a preset channel backward (effective when Function is set to TV)
Numeric keys	Function: TV	Function: TV (previously selected channel number is selected)
POWER	Power OFF	Turning the power off
FACTORY	Factory OFF	Turning Service Factory mode off
MENU	Menu ON	Turning Service Factory mode off and Menu mode on



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25

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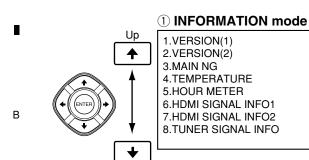
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^{*} During step 3 to 8, if other operations took place, the Service Factory routine is cleared.

■ Changes of the Service Factory menus



Down



6 INITIALIZE mode

1.SYNC DET
2.SG MODE
3.SG PATTERN
4.SIDE MASK LEVEL
5.FINAL SETUP
6.SR+
7.UART SELECT
8.CVT AUTO
9.HDMI INTR POSITION





2 FUNCTION CHECK mode





1.PEAK LIMITER 2.EDID WRITE MODE 3.CH PRESET





③ COMMON ADJ. mode

1. RGB 1



4 PANEL FACTORY mode

1.PANEL INFORMATION
2.PANEL WORKS
3.POWER DOWN
4.SHUT DOWN
5.PANEL-1 ADJ
6.PANEL-2 ADJ
7.PANEL REVICE
8.ETC
9.MASK SETUP

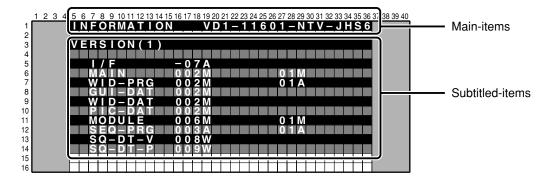
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26

PDP-R06G

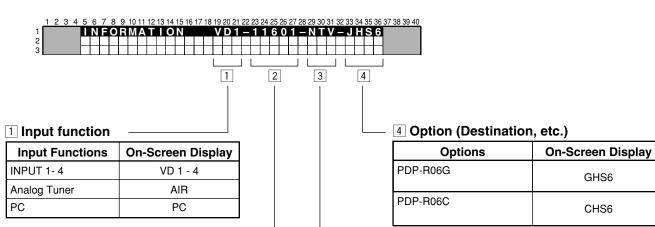
■ Indications in Service Factory mode



■ Main-item indications

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Four parameters are displayed:



2 SIG mode and screen size

Note: See SIG-Mode Tables. (See next page.)

3 Color system and signal type

5

Color System and Signal Type		On-Screen Display	Color System and Signal Type		On-Screen Display
NTSC		NTV	NTSC		NTS
PAL		PLV	PAL		PLS
PAL N		PNV	PAL N		PNS
PAL M	Composite input	PMV	PAL M	S-connector input	PMS
SECAM		SCV	SECAM		SCS
4.43NTSC		4NV	4.43NTSC		4NS
BLACK/WHITE		BWV	BLACK/WHITE		BWS
Y/CB/CR		CBR	RGB		RGB
Y/PB/PR		PBR	Digital video signal		DIG

27

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GHS6

CHS6

● SIG-Mode Table

В

The signal mode is displayed in four charecters:

1st and 2nd charecters: Resolutin of the input signal (numerics for the video signals, and alphabetics for the PC signals)

3rd and 4th charecters: Grouping of the V frequencies (refresh rate)

5th charecter : Selection of the screen size by the user is displayed.

SIG-Mode table for video signals (resolutions and V frequencies)

1st and 2nd	3rd and 4th	Signal Type	Fv (Hz)	Fh (kHz)
10	50	SDTV*625i	50.000	15.625
10	60	SDTV*525i	60.000	15.750
12	60	SDTV*525i (PAL60)	60.000	15.750
00	50	SDTV*625p	50.000	31.250
20	60	SDTV*525p	60.000	31.500
00	50	HDTV*1125i	50.000	28.125
30	60	HDTV*1125i	60.000	33.750
40	50	HDTV*750p	50.000	37.500
40	60	HDTV*750p	60.000	45.000
50	24	HDTV*1125p	24.000	27.000

Fv: Vertical Frequency, Fh: Horizontal Frequency

SIG-Mode table for PC signals (resolutions and V frequencies)

1st and 2nd	3rd and 4th	Signal Type	Fv (Hz)	Fh (kHz)
C1	70	720x400	70.087	31.469
	60		59.940	31.469
C2	72	640x480	72.809	37.861
	75		75.000	37.500
	56		56.250	35.1556
C4	60	800x600	60.317	37.879
04	72		72.188	48.077
	75		75.000	46.875
	60		60.004	48.363
C7	70	1024x768	70.069	56.476
	75		75.029	60.023
	56		56.250	45.113
C8	60	1280x768	59.833	47.986
	70		70.000	56.137

Fv: Vertical Frequency, Fh: Horizontal Frequency

Selection of the screen size by the user is displayed.

	or and out of the state of the			
5th	Description on GUI	VIDEO	PC	Remarks
0	DOT BY DOT	-	•	
1	4:3	•	•	
2	FULL(FULL1)	•	•	
3	ZOOM	•	_	
4	CINEMA	•	_	
5	WIDE	•	_	
6	FULL 14:9	•	-	
7	CINEMA 14:9	•	_	
8	FULL2	•	•	

●: available, -: not available

28

PDP-R06G

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■ Factory Menus

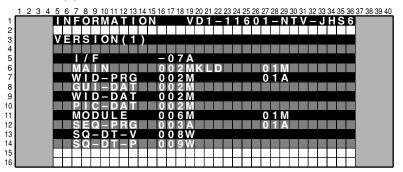
① INFORMATION mode

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Operation items

No.	Function / Display	Content	RS-232C
1	VERSION (1)	The flash memory versions for each device are displayed. (common part)	QS1
2	VERSION (2)	The flash memory versions for each device are displayed. (individual part)	QS6
3	MAIN NG	The shutdown generated on Media Receiver side and its time of occurrence are displayed.	QNG
4	TEMPERATURE	Information of temperature and fan status on Media Receiver side are displayed.	QMT
5	HOUR METER	Cumulative power-on time to the Media Receiver is displayed.	-
6	HDMI SIGNAL INFO 1	The file information of LIDAN environment displayed	_
7	HDMI SIGNAL INFO 2	The file information of HDMI series are displayed.	
8	TUNER SIGNAL INFO	The signal information on TUNER is displayed.	-

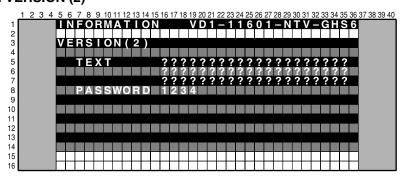
1. VERSION (1)



Flash memory on Device	On-Screen Display
IF microcomputer	I/F
Main microcomputer	MAIN
Program for CARRERA-MANTA	WID-PRG
GUI data for CARRERA-MANTA	GUI-DAT
Enhanced data for CARRERA-MANTA.	WID-DAT
Picture Quality data for CARRERA-MANTA	PIC-DAT
Module microcomputer (for the PDP)	MODULE
Program for ASTRA-MANTA (for the PDP)	SEQ-PRG
Sequence data for ASTRA-MANTA Video	SQ-DT-V
Sequence data for ASTRA-MANTA PC	SQ-DT-P

2. **VERSION** (2)

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Device	On - Screen Display	Version Display	Remarks
Teletext ucom Software Version	TEXT	60 character	20 character x 3
User Password	PASSWORD	4 character	

29

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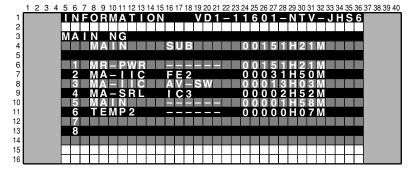
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PDP-R06G

3. MAIN NG

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• Media Receiver NG information

OSD: MAIN	OSD: SUB	Cause of Shutdown	
MODULE		Abnormally in Module microcomputer communication	
MA-SRL		Abnormally in 3-wire Serial Communication of the Main microcomputer.	
	IF	Communication failure of IF microcomputer	
	MULTI1	MANTA communication failure (MULIT1)	
	I/P	MANTA communication failure (I/P)	
	D-SEL	MANTA communication failure (D-SEL)	
MA-IIC		Abnormally in Main microcomputer IIC communication	
	FE1	Analog Tuner 1 (Front End 1)	
	AV-SW	AV Switch	
	RGB-SW	RGB Switch	
	M-VDEC	Main VDEC	
	S-VDEC	Sub VDEC	
	ADC	AD/PLL	
	HDMI	HDMI	
	PLK-T	TMDS Tx	
	PLK-R	TMDS Rx	
	TX-COM	M2 Communication	
	TX-BSY	M2 Busy	
	MA-EEP	64k EEPROM	
MAIN		Abnormally in Main microcomputer communication	
FAN		Fan stopped	
TEMP2		Abnormally high temperature of the MR.	

30

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PDP-R06G

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4. TEMPERATURE

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40

INFORMATION VD1-11601-NTV-JHS6

TEMPERATURE

TEMP2: 13 0

FAN: MIN

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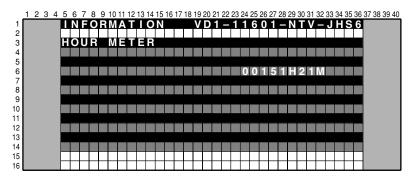
TEMP2: The value read from the temperature sensor built into the Media Receiver is displayed in the range of 000-255. For reference, the approximate value for 60°C is 86 and for 35°C is 67.

Reference: When TEMP2 exceeds 100 (about 78°C), SD LED (Blue) flash 11 times.

FAN: The value of the Fan output is displayed.

STOP: stopped, MIN: slow speed, MAX: high speed

5. HOUR METER



The cumulative power-on time of the Media Receiver is displayed.

31

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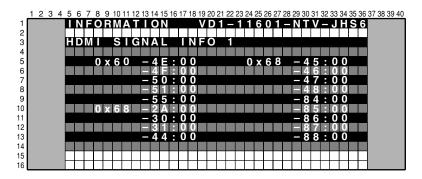
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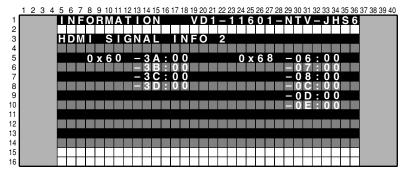
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PDP-R06G

6. HDMI SIGNAL INFO

В





• Technical examination display (Reading status registers in HDMI receiver and displaying them by HEX value.)

	HDMI SIGNAL INFO 1				
	SA Context				
	- 4E:	Video DE pixels [7:0]			
	- 4F:	Video DE pixels [11:8]			
0x60	- 50:	Video DE lines [7:0]			
	- 51:	Video DE lines [10:8]			
	- 55:	Video status (interlace or progressive, sync polarity)			
	- 2A:	Audio in channel status (PCM, copy information etc.)			
	- 30:	Audio in SPDIF channel status (sampling frequency)			
	- 31:	Audio in SPDIF channel status (sample word length)			
	- 44:	AVI InfoFrame data1 (video format etc.)			
	- 45:	AVI InfoFrame data2 (colorimetry, aspect ratio)			
	- 46:	AVI InfoFrame data3 (video scaling)			
0x68	- 47:	AVI InfoFrame data4 (video identification code)			
	- 48:	AVI InfoFrame data5 (pixel repeat value for 2880dot)			
	- 84:	Audio InfoFrame data1 (channel count, cording type)			
	- 85:	Audio InfoFrame data2 (always zero)			
	- 86:	Audio InfoFrame data3 (always zero)			
	- 87:	Audio InfoFrame data4 (channel / speaker allocation)			
	- 88:	Audio InfoFrame data5 (downmix inhibit, level shift value for downmixing)			

32

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PDP-R06G

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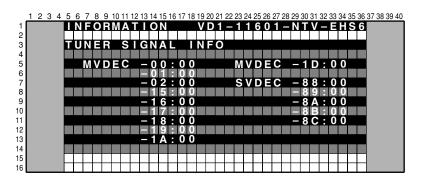
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	HDMI SIGNAL INFO 2			
Ş	SA	Context		
	- 3A:	Video full H resolution [7:0]		
0,400	- 3B:	Video full H resolution [12:8]		
0x60	- 3C:	Video full V lines [7:0]		
	- 3D:	Video full V lines [10:8]		
	- 06:	N Value for audio clock regeneration method. [7:0]		
	- 07:	N Value for audio clock regeneration method. [15:8]		
000	- 08:	N Value for audio clock regeneration method. [19:16]		
0x68	- 0C:	CTS Value for audio clock regeneration method. [7:0]		
	- 0D:	CTS Value for audio clock regeneration method. [15:8]		
	- 0E:	CTS Value for audio clock regeneration method. [19:16]		

7. TUNER SIGNAL INFO

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• Tuner signal information in MVDEC / SVDEC.

Device	SA	Context
	00h	Signal distinction 1
	01h	Signal distinction 2
	02h	Flag detection output
	15h	Noise level detection 1
MVDEC	16h	Noise level detection 2
MVDEC	17h	Non - standard signal detection
	18h	Subcarrier signal detection
	19h	ACC data output
	1Ah	ACC information output
	1Dh	Input signal mode
	88h	Status register 1 (TV/VCR status)
	89h	Status register 2 (Macrovision detection etc)
SVDEC	8Ah	Status register 3 (Front-end AGC gain value)
	8Bh	Status register 4 (Subcarrier to horizontal (SCH) phase)
	8Ch	Status register 5 (signal distinction)

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PDP-R06G

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2 3 **4**

② FUNCTION CHECK mode

Operation items

No.	Display	Content	RS-232C
1	FAN <=>	Control FAN speed for Force.	_
2	AFT <=>	Control AFT Lock.	_

③ COMMON ADJ. mode

RGB1

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Only for the technical use.

4 PANEL FACTORY mode

Operation items

No.	Function / Display		
1	PANEL INFORMATION		
2	PANEL WORKS		
3	POWER DOWN		
4	SHUT DOWN		
5	PANEL-1 ADJ		
6	PANEL-2 ADJ		
7	PANEL REVISE		
8	ETC		
9	MASK SETUP		

Refer to the service manual of the PDP-506P/436P.

5 OPTION mode

Operation items

No.	Function/Display	Content	RS-232C
1	PEAK LIMITTER ⇔	Control Peak Limitter (Select ON/OFF)	_
2	EDID WRITE MODE ⇔	Control EDID WRITE MODE (Select DISABLE/ENABLE)	_
3	CH PRESET ⇔	Only for the productical use.	_

34 PDP-R06G

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6 INITIALIZE mode

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Operation items

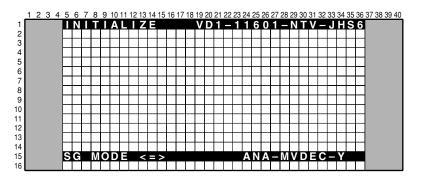
No.	Function/Display	Content	RS-232C
1	SYNC DET (+)	Only for the technical use.	_
2	SG MODE ⇔	Paired SG_MODE with SG_PATTERN. Select SG Route.	_
3	SG PATTERN ⇔	Paired SG_MODE with SG_PATTERN. Select SG Pattern.	_
4	SIDE MASK LEVEL (+)	Adjust Side Mask Color (R, G, B).	BSL GSL RSL
5	FINAL SETUP (+)	Initialize flash memories on virgin product status	FST
6	SR+ ⇔	Select SR+ mode or UART SELECT mode.	_
7	UART SELECT ⇔	Select boud Rate on RS-232C Communication	_
8	CVT AUTO ⇔	Only for the productical use.	_
9	HDMI INTR POSITION (+)	Only for the technical use.	_

1. SYNC DET(+)

Only for the technical use.

2. SG MODE

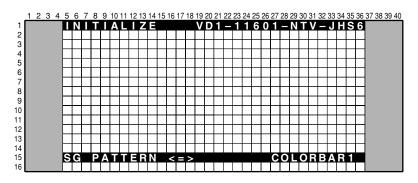
The route of the Test Signal from the MVDEC is chosen by this function. After setting this function, SG pattern should be set.



No.	Display	Function	
1	SG OFF	SG is set to OFF	
2	DIG MVDEC YCBCR	Digital output (YCbCr)	
3	ANA MVDEC RGB	Analog output to the RGB SW (RGB)	
4	ANA SVDEC Y	Analog output to the Video SW (Y)	
5	ANA MVDEC Y	Analog output to the Video SW (Y)	
6	ANA AD YCBCR	Analog output to the RGB SW (YCbCr)	
7	ANA AD RGB	Analog output to the RGB SW (RGB)	

3. SG PATTERN

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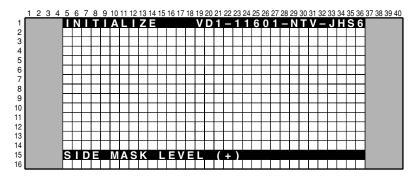
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Α	No.	Function/Display	SG Pattern (Brightness IRE Level/Color)	No.	Function/Display	SG Pattern (Brightness IRE Level/Color)
	1	COLOR BAR1	Colorbar (75%)	11	RASTER4	Raster (75% Green)
	2	COLOR BAR2	Colorbar (100%)	12	RASTER5	Raster (75% Magenta)
	3	RAMP1	Ramp (100% white)	13	RASTER6	Raster (75% Red)
•	4	RAMP2	Ramp (100% Yellow)	14	RASTER7	Raster (75% Blue)
	5	RAMP3	Ramp (75% Green)	15	RASTER8	Raster (-% Black)
	6	RAMP4	Ramp (75% Red)	16	10STEP1	10STEP (100% white)
	7	RAMP5	Ramp (75% Blue)	17	10STEP2	10STEP (100% Yellow)
В	8	RASTER1	Raster (100% White)	18	10STEP3	10STEP (75% Green)
	9	RASTER2	Raster (75% Yellow)	19	10STEP4	10STEP (75% Red)
İ	10	RASTER3	Raster (75% Cyanide)	20	10STEP5	10STEP (75% Blue)

Important notice of the Test Signal mode (SG mode, SG pattern)

- The route switching should be done correctly in the factory mode.
- Y or G signal from SG should be input to the AVI terminal of the MVDEC when the SG signal is output.
- The function of the blanking offset (50 IRE) should be OFF during the SG mode.
- The setting of the Y/C separation function should be set to the NTSC during the SG mode
- Only the RGB and Component signals can be output during SG mode, so only the Y signal is input at the CVBS and S signal mode, thus the picture is composed in black and white color. This isn't a trouble.
- The SG mode 7 (ANA AD RGB) is only for the factory mode. Therefore some probrem (strange color, unstable brightness etc.) might be happened.

4. SIDE MASK LEVEL



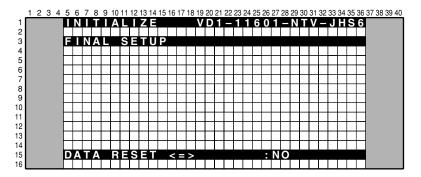
Level of the side mask (R, G, and B) can be adjusted by using this menu. The input signal is necessary to adjust it.

No.	Display Context		RS-232C
1	R MASK LEVEL ⇔	Adjust Side Mask R (range :000-255)	RSL
2	G MASK LEVEL ⇔	Adjust Side Mask G (range :000-255)	GSL
3	B MASK LEVEL ⇔	Adjust Side Mask B (range :000-255)	BSL

36

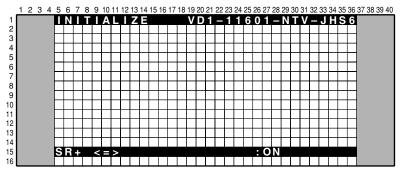
Ε

5. FINAL SETUP



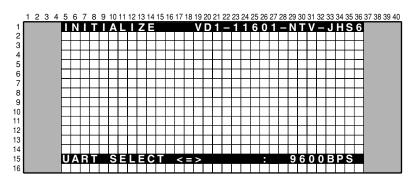
The value of all memorized data are set to shipment status. If the ENTER key is kept on pressing for 5 second when the status of this menu is YES, final setup will be done.

6. SR+



SR+ function \rightarrow ON, RS-232C function \rightarrow OFF

7. UART SELECT



This function can be selected when the SR+ function is OFF.

Option No.	Display	Operation / Control	RS-232C
1 (Initial setting)		To Set to SR+ (9600bps)	SR+ is ON
2	1200	To Set to RS-232C (1200bps)	SR+ is OFF
3	2400	To Set to RS-232C (2400bps)	SR+ is OFF
4	4800	To Set to RS-232C (4800bps)	SR+ is OFF
5	9600	To Set to RS-232C (9600bps)	SR+ is OFF
6	19200	To Set to RS-232C (19200bps)	SR+ is OFF
7	38400	To Set to RS-232C (38400bps)	SR+ is OFF

37

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6.4 LIST OF RS-232C COMMANDS

RS-232C commands can be used in Service Factory mode. Before using RS-232C commands, it is necessary to change the factory presetting.

Also the RS-232C commands for the panel is not listed. Please refer to panle's service manual.

Command	Operation	Remarks
В		
BSL	Adjust side mask B	
С		
CNG	Clearing MR NG information	
CHR	Clearing MR Hour meter	
D		
DW*	Decreasing the adjustment value by*	*:1-9, 0 (0 means 10), F (making the adjustment value the minimum)
	200.000g the adjustment value by	. To, o (o modulo 10), . (making allo dajdomon, taldo allo milimidin)
F		
FAN	Turning Service Factory mode off.	
FAY	Turning Service Factory mode on.	
	running corrider actory mode cin	
FST	Final Set Up	
G		
GSL	Adjusting side mask G	
1	, ,	
INA	Selection of tuner for terrestrial analog signals.	
INC***	Selection of tuner for terrestrial digital signals.	
INH	Selection of SD card/PCMCIA card	
INPS01	Input selection: input 1	
INPS02	Input selection: input 2	
INPS03	Input selection: input 3	
INPS04	Input selection: input 4	
INPS05	Input selection: input 5	
	·	
0		
OSDS00	Turning On-Screen Display off	Prohibit On-Screen Display.
OSDS01	Turning On-Screen Display on	Permit On-Screen Display.
Р		
POF	Turning the power off.	
PON	Turning the power on.	
Q		
QS1	Obtaining the version data for each device.	
QS6	Obtaining the any version.	
QMT	Obtaining the MR temperature information.	
QNG	Obtaining NG data of the MR.	
R		
RSL	Adjustment od side mask R	
U		
UP*	Increasing the adjustment value by *	*:1-9, 0 (0 means 10), F (making the adjustment value the maximum)
Z		
ZME	Initializing of the EEPROM video data	
	<u> </u>	

38

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PDP-R06G

6.5 OUTLINE OF COMMANDS

If you want to see version information (ex. QS1,QS6,Factory Menu), Please see 10 seconds after starting.

QS1: Returning information on the module and the version of the software.

Order	Part	Data Content	Size	Remarks
0	-	Received Command Name on MR	3 byte	'QS1' only
1		Display Information 1	1 byte	
2		Display Information 2	1 byte	
3		Display Information 3	1 byte	
4		Display Information 4	1 byte	
5		Display Information 5	1 byte	
6		Boot Version of Module microcomputer.	3 byte	
7	MDU	Program Version of Module microcomputer.	8 byte	
8		Boot Version of ASTRA-MANTA	3 byte	
9		Program Version of ASTRA-MANTA	8 byte	
10		Sequence Version (43VIDEO)	4 byte	
11		Sequence Version (43PC)	4 byte	
12		Sequence Version (50VIDEO)	4 byte	
13		Sequence Version (50PC)	4 byte	
14		, (comma)	1 byte	
15		MR Information 1	1 byte	
16		MR Information 2	1 byte	
17		MR Information 3	1 byte	
18		MR Information 4	1 byte	
19	MD	Version of IF microcomputer	4 byte	
20	MR	Version of Main microcomputer	8 byte	
21		Boot Version of Main microcomputer	4 byte	
22		Program Version of CARRERA-MANTA	8 byte	
23		Boot Version of CARRERA-MANTA	4 byte	
24		GUI Version of CARRERA-MANTA	8 byte	
25		Enhanced Version of CARRERA-MANTA	8 byte	
26		PIC Version of CARRERA-MANTA	8 byte	

QS6: Returning information of the Flash Device.

Order	Data Content	Size	Remarks
0	Received Command Name on MR	3 byte	'QS6' only
1	Version of Text	60 byte	
2	User Password	4 byte	

QMT: Returning information of MR temperature and FAN speed.

Order	Data Content	Size	Remark
1	Received Command Name on MR	3 byte	'QMT' only
2	MR Temperature	3 byte	
3	MR FAN Speed	1 byte	0: STOP 1: MIN 2: MAX

39

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QNG: Returning data (logs keep on Main microcomputer) on shutdown of Media Receiver.

3

Order	Data		Context
0	Received Command Name on MR	3 byte	'QNG' only
1	Latest NG data	1 byte	
2	Data of subcategory for the latest NG	1 byte	
3	Data of MR hour meter for the latest NG	7 byte	
4	Data of temperature for the latest NG	3 byte	
5	2nd latest NG data	1 byte	
6	Data of subcategory for the 2nd latest NG	1 byte	
7	Data of MR hour meter for the 2nd latest NG	7 byte	
8	Data of temperature for the 2nd latest NG	3 byte	
:	:	:	
29	7th latest NG data	1 byte	
30	Data of subcategory for the 8th latest NG	1 byte	
31	Data of MR hour meter for the 8th latest NG	7 byte	
32	Data of temperature for the 8th latest NG	3 byte	

• Details on the NG data and subcategory

Data	Cause of Shutdown	Remarks
0	Normal	
1	Failure of communication to Module microcomputer	
2	3-wire Serial Communication of Main microcomputer.	Subcategory ⇒ 1
3	IIC Communication failure of Main microcomputer	Subcategory \Rightarrow 2
4	Communication failure of Main microcomputer &Unknown Error	
5	Fan stopped	
6	Abnormally high temperature at MR.	
8	Abnormally in RST2 of MR (power decrease of DC-DC converter)	

• Data on Subcategories for failure in 3-wire serial communication of Main microcomputer (subcategory 1)

Data	Cause of Shutdown	Remarks
0	Non subcategory	
1	Communication failure of IF microcomputer	Power OFF
2	MANTA communication failure (MULIT1)	Power OFF
3	MANTA communication failure (MULIT2)	Reserved
4	MANTA communication failure (I/P)	
5	MANTA communication failure (D-SEL)	

• Data on Subcategories for failure in IIC communication of Main microcomputer (subcategory 2)

Data	Cause of Shutdown	Data	Cause of Shutdown
0	Non subcategory	Α	AD/PLL
1	Analog Tuner 1 (Front End 1)	В	НДМІ
4	AV Switch	С	TMDS Tx
5	RGB Switch	D	TMDS Rx
8	Main VDEC	Е	M2 Communication
9	Sub VDEC	F	M2 Busy
		G	64k EEPROM

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PDP-R06G

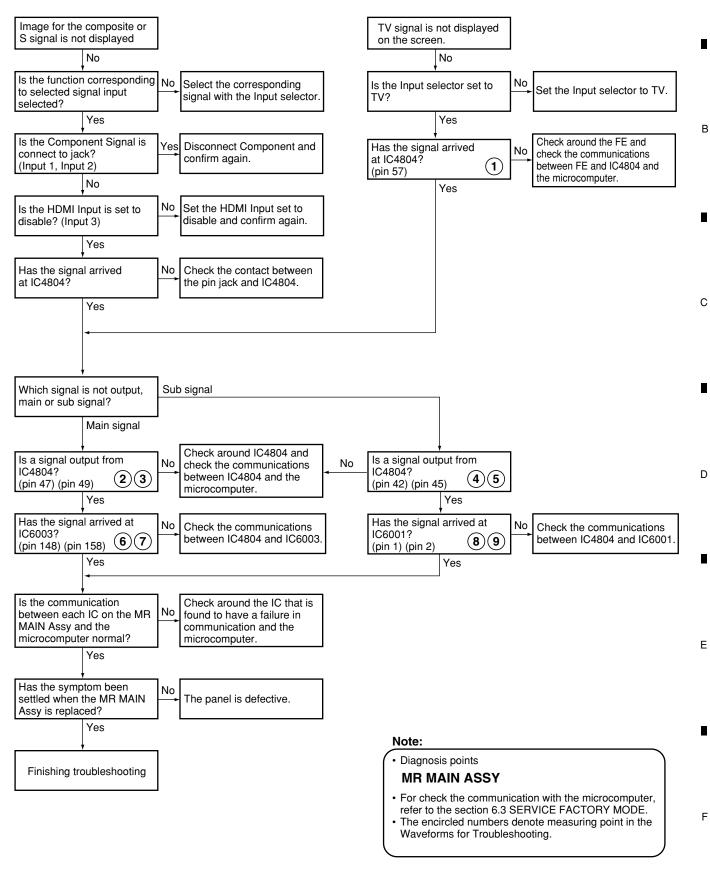
7. GENERAL INFORMATION

7.1 DIAGNOSIS

7.1.1 TROUBLESHOOTING

5

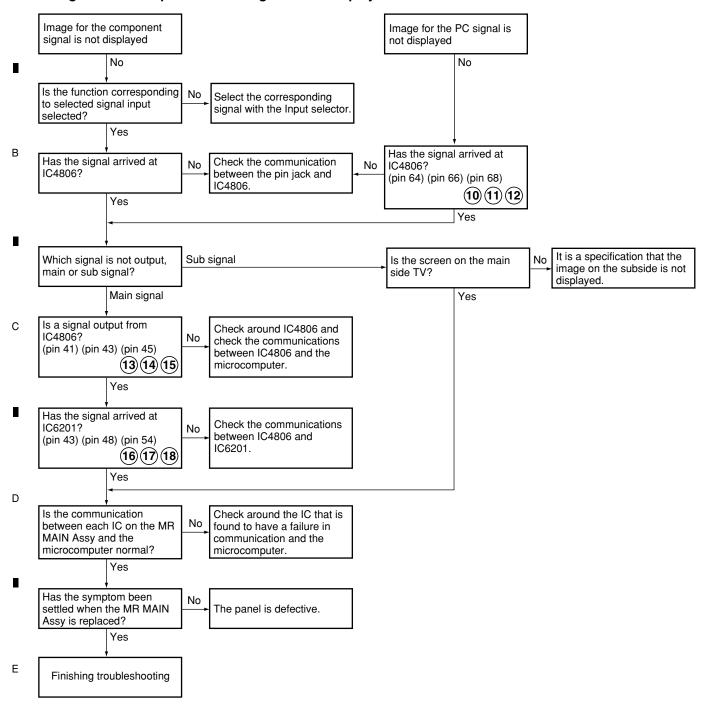
Image for the composite or S or TV signal is not displayed



PDP-R06G

41

• Image for the component or PC signal is not displayed



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42

PDP-R06G

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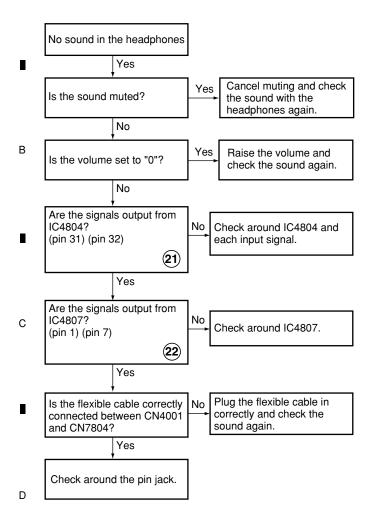
43

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2 3 4

No sound from the headphones and audio output



44

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PDP-R06G

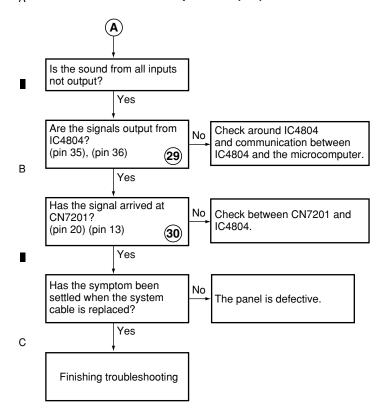
В

С

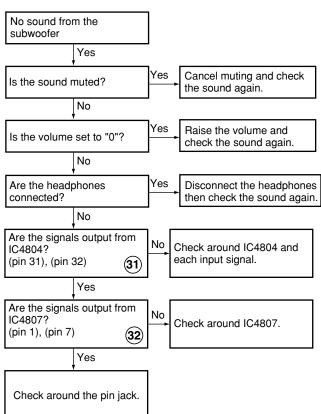
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No sound from the speakers (2/2)



No sound from the subwoofer



46

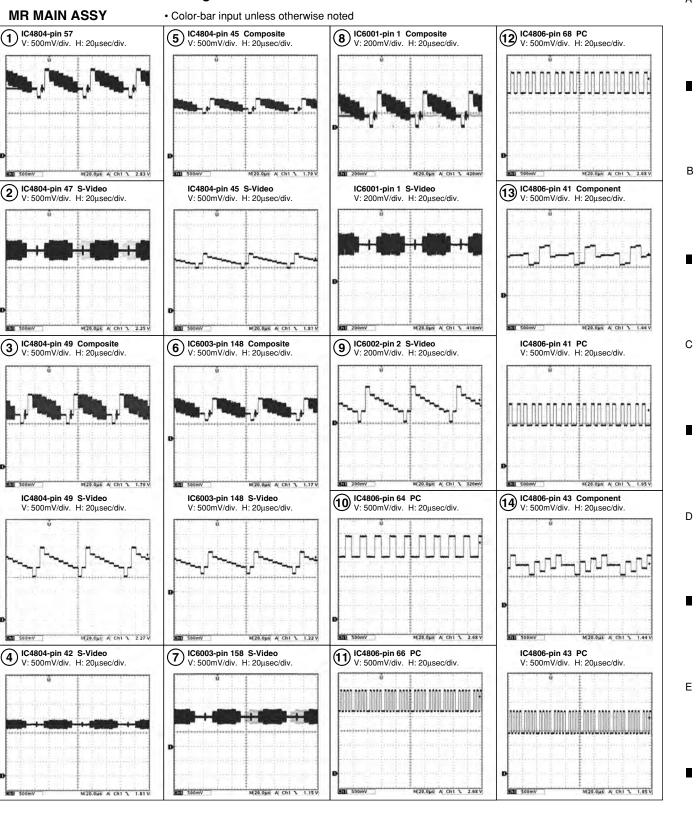
Ε

PDP-R06G

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Waveforms for Troubleshooting



PDP-R06G

47

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1 2 3 4

· Color-bar input unless otherwise noted

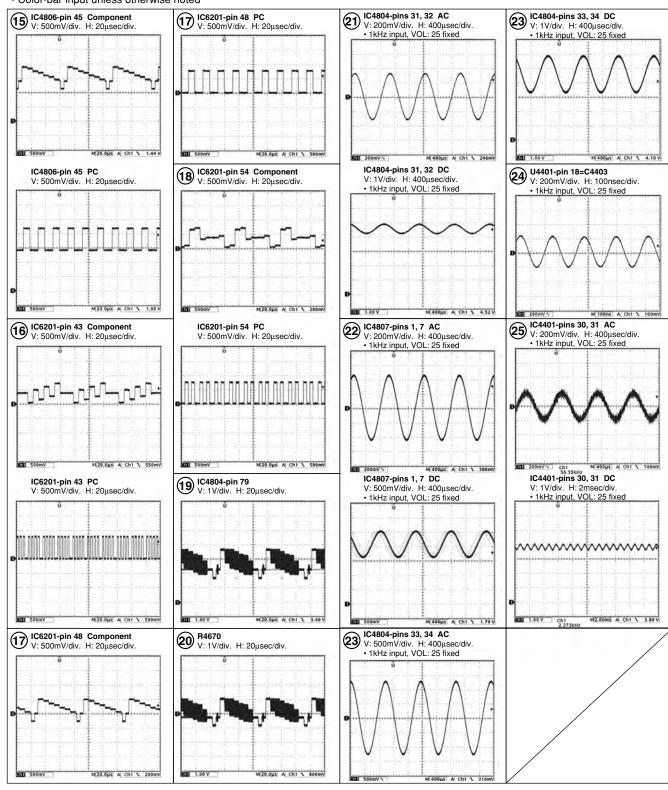
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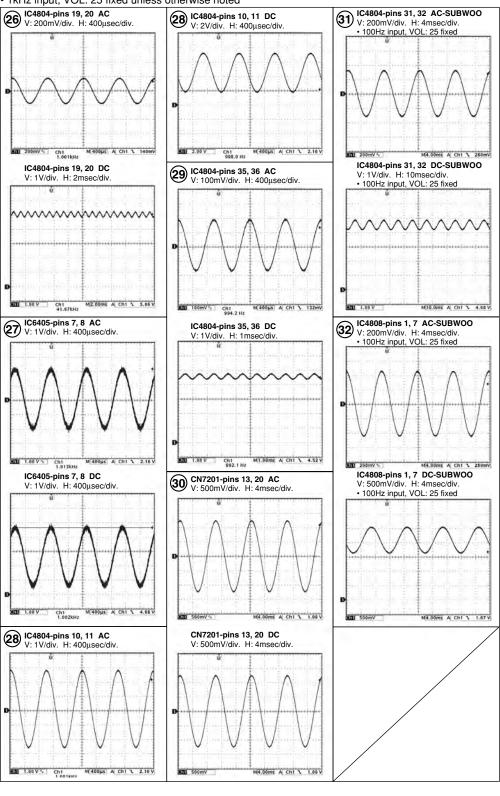
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48

PDP-R06G

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• 1kHz input, VOL: 25 fixed unless otherwise noted



49

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PDP-R06G

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7.1.2 DISASSEMBLY

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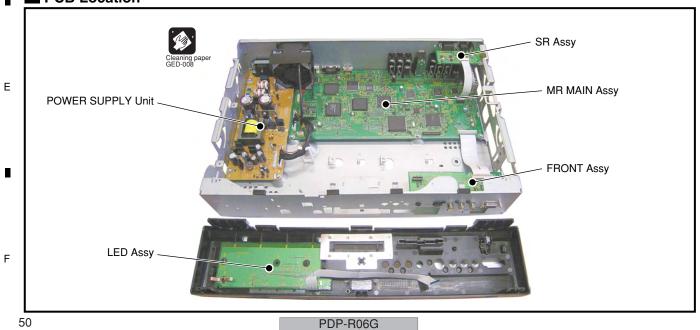
С

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Note : Even if the unit shown in the photos and illustrations in this manual may differ from your product, the procedures described here are common.

Remove the four screws. Remove the metal bonnet while pulling it backward. Metal bonnet Pront Panel Section Disconnect the flexible cable. Remove the flexible cable from the flat clamp. Unhook the six hooks. Remove the four screws. Remove the four screws. Remove the metal bonnet while pulling it backward. Metal bonnet Pront panel section Front panel section Bottom view

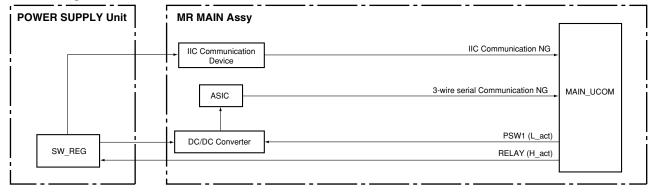
PCB Location



7.2 EXPLANATION 7.2.1 PROCESSING IN ABNORMALITY

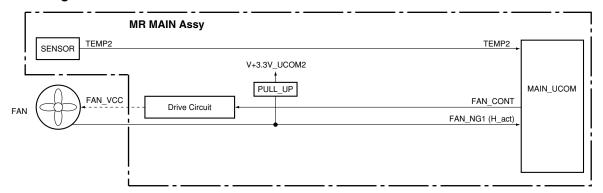
Power supply and DC-DC converter

Circuit diagram



Fan and temperature sensor

Circuit diagram



Specifications for port monitoring

Port Name	SD/PD Indication	Assigned Pin	Active
FAN_NG 1	FAN	155	Shutdown with H
TEMP2	Abnormally high temperature in the MR	76	Shutdown when the value exceeds the predetermined value

51

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2.5sec 2.5sec 1sec **LED-lighting Pattern** 100msec 1sec 50msec 50msec 0.5sec 0.5sec 100msec В α а с В α **ш** m m ш ш **ш** В α Flashing alternately in red and blue (at 1-sec intervals) Flashing in blue n times (initially at 0.5-sec intervals then 2.5-sec intervals) Flashing in red for n times (initially at 0.5-sec intervals then 2.5-sec intervals) Flashing in red (at 1-sec intervals) Lit in blue Status of the Unit Lit in red System cable disconnected * | Shutdown (circuit protection) Waiting for finish of rewriting by the microcomputer Waiting for start of rewriting by the microcomputer Standby, power management PDP's power not on Power-down (circuit protection) Power on

* In this case, the red and blue areas on the screen of the panel flash alternately.

52

■ LED-lighting patterns

1

2

PDP-R06G

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■ Defective points assumed from the number of times of LED flashing

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EDs on t	LEDs on the panel LEDs on the MR	ים מכודו		VIOCOTO .	Site detected as		OSD WHEN delected
	i	בני כ	The MH	*1	defective	Possible defective points (representative examples)	(warning message)
Red	Blue	Red	Blue		:	c	
1	Blue 1	Red			Panel drive IC	*2	None
	Blue 2	Red			Module section IIC	*2	None
	Blue 3	Red			Power decrease of DIGITAL-DC-DC	*2	None
	Blue 4	Red			Panel having abnormally high temperature	*2	Power off. Internal temperature is too high. Check temperature around PDP. [SD04] *6
	Blue 5	Red			Short-circuiting of the speakers	*5	Internal protection circuit turns power off. Is there a short in speaker cable? (SD05)
Red			Blue 6		Module microcomputer	Disconnection of the system cable Defective module microcomputer or its peripheral circuits of the panel (Refer to the service manual of the PDP-438PG or PDP-508PG.) Defective main microcomputer (ICS206) Failure in communication (TXD_MD, RXD_MD, REQ_MD) between the panel's module microcomputer and IC5206 (main microcomputer)	None
Red			Blue 7		3-wire serial connection of the main section	Defective ICS002 or its peripheral circuits Fallure in communication (TXD_IF, RXD_IF, CLK_IF, CE_IF, REQ_IF, BUSY_IF) between ICS002 and ICS206 (main microcomputer) Defective (C7001 or its peripheral circuits Fallure in communication (TXD_IC3, RXD_IC3, CLK_IC3, CE_IC3, REQ_IC3, BUSY_IC3) between IC7001 and IC5206 (main microcomputer)	None
Red			Blue 8	S	IIC of the main section	Defective UA401 (FF1) or its peripheral circuits Defective IC4401 (MPX) or its peripheral circuits Defective IC4501 (MPX) or its peripheral circuits Defective IC4604 (MPX) or its peripheral circuits Defective IC5005 (MCVEC) or its peripheral circuits Defective IC6001 (S-VDEC) or its peripheral circuits Defective IC6001 (ADC) or its peripheral circuits Defective IC5007 (MPC) or its peripheral circuits	None
						Failure in communication (SCL_AV, SDA_AV, SCL_MA, SDA_MA, SCL_EP, SDA_EP, SCL_TTX, SDA_TTX, SCL-HDCP, SDA-HDCP) between one of the above devices and IC5206 (main microcomputer)	
Red			Blue 9		Main microcomputer	Defective IC5206 (main microcomputer) Failure in communication (TXD_IF, RXD_IF, CLK_IF, CE_IF, REQ_IF, BUSY_IF) between IC5206 (main microcomputer) and IC5002	None
Red			Blue 10		Fan	Failure in the fan motor, or the fan stopped because of dust attached to the fan	None
Red			Blue 11		MR or unit having abnormally high temperature	The Media Receiver or the unit being used at high temperature	Power off. Internal temperature is too high. Check temperature around media receiver. [SD011]
Red			Blue 12		Digital tuner	Defective DTV tuner *5	None
Red			Blue 13		ASIC power supply (DC-DC)	Defective U4201 (DD_CON) or short-circuiting elsewhere *6	None
Red 2		Red			POWER	% 2 **	None
Red 3		Red			SCAN	*2	None
Red 4		Red			SCN-5V		None
Red 6		Bed			Y-DCDC	~	None
Red 7		Red			X-Sus		None
Red 8		Red		<u>Q</u>	ADRS	I &	None
Red 9		Red			X-DRV	*2	None
Red 10		Red			X-DCDC	Z *	None
Red 11		Red			X-SUS	*2	None
Red 12		Red			D-DCDC	Z _%	None
Red 13		Ped C			IC4	7 C	None
Ked 15		Hed 15 Hed Hed Hed Hed Hed Hed Hed 15 Hed			UNKNOWN	None	None

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PDP-R06G

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E

REM Power MOD infrared STB Control receiver Power MOD MOD **RELAY Control** Microcomputer BUFF BUFF BUFF REM_B MR BUFF IC7203 MTXD MRXD **BUFF** Keys on the REQ IC7203 front panel STB MT BUFF IC7201 KEY_AD2 Inverter Q5001 4 ACTIVE SR IN PM SW (1)② IC4212 IF_UCOM IC5002 V+3 3V UCOM SR Circuit MAIN UCOM Power MOD Q7603-Q7606 (3) 6) RELAY Control IC5206 TXD_IF RXD_IF CE_IF REQ_IF BUSY_IF

REM_B Descriptions in a call-out are signal names for reference.

For wiring numbers on the PDP side, refer to the service manual for the PDP.

- $\ensuremath{\textcircled{1}}$: The signal from the remote control unit (or a key signal) is input to the IF microcomputer.
- ②: The IF microcomputer supplies the power to the main microcomputer and MOD microcomputer.
- ③: The IF microcomputer transmits operation data from the remote control unit (or keys) to the main microcomputer.
- ④: The main microcomputer issues a startup command to the MOD microcomputer.
- ⑤: The MOD microcomputer controls the relay of the PDP Power MOD and starts the power-on sequence of the PDP.
- 6 : The main microcomputer controls the relay of the MR Power MOD and starts the power-on sequence of the MR.

PDP-R06G

54

3

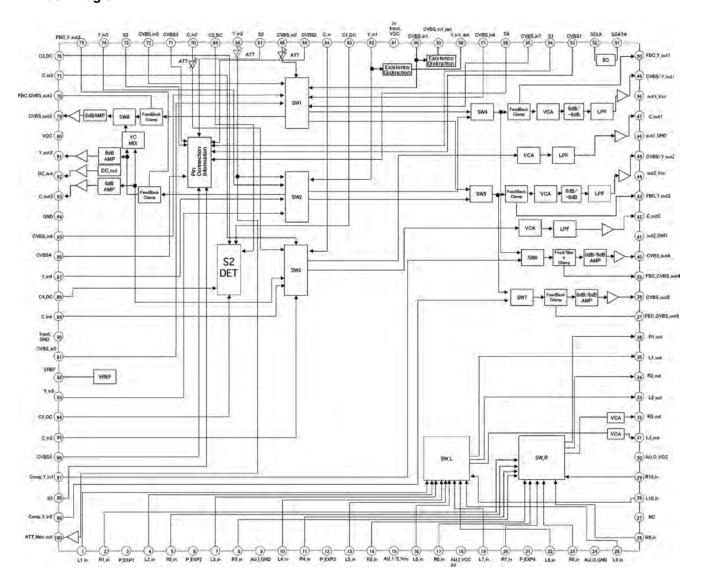
- The information shown in the list is basic information and may not correspond exactly to that shown in the schematic diagrams.
- List of IC

R2S11002AFT, R2S11001FT, K4S641632H-TC75, S29AL016D70TFI010, UPD64015GM-UEU, TVP5150AM1PBS, K4S161622H-TC60, AD9985KSTZ-110, SII9021CTU, K4S643232H-TC60, MBM29DL162TE70TN, SII170BCLG64, AXF1150, AXY1117

■ R2S11002AFT (MR MAIN ASSY: IC4804)

- AV SW
- Block Diagram

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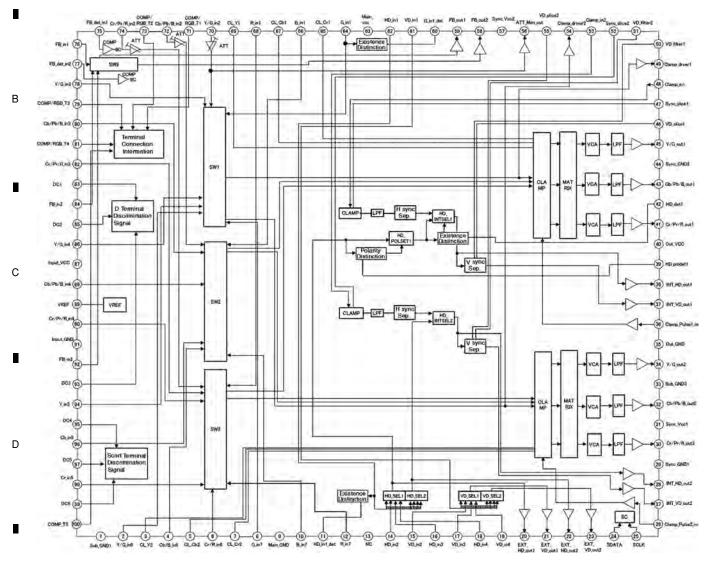
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■ R2S11001FT (MR MAIN ASSY: IC4806)

· Component SW IC

Block Diagram



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56

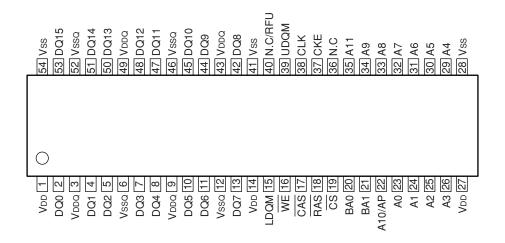
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PDP-R06G

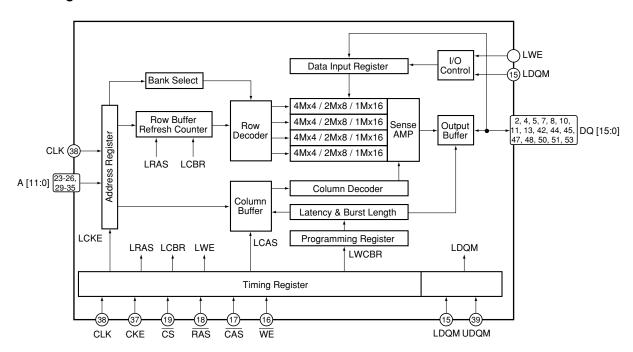
■ K4S641632H-TC75 (MR MAIN ASSY : IC5403)

• 64M SDRAM

Pin Arrangement (Top view)



Block Diagram



57

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PDP-R06G

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2 - 3 - 4

Pin Function

В

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No.	Pin Name	I/O	Pin Function	No.	Pin Name	I/O	Pin Function
1	VDD	_	Power supply	28	Vss	-	Ground
2	DQ0	I/O	Data input/output	29	A4	I	Address input
3	VDDQ	_	Power supply for data output	30	A5	I	Address input
4	DQ1	I/O	Data input/output	31	A6	_	Address input
5	DQ2	I/O	Data input/output	32	A7	Ι	Address input
6	VssQ	_	Ground for data output	33	A8	Ι	Address input
7	DQ3	I/O	Data input/output	34	A9	I	Address input
8	DQ4	I/O	Data input/output	35	A11	I	Address input
9	VDDQ	_	Power supply for data output	36	N.C	_	No connection
10	DQ5	I/O	Data input/output	37	CKE	I	Clock enable input
11	DQ6	I/O	Data input/output	38	CLK	I	System clock input
12	Vssq	_	Ground for data output	39	UDQM	I	Data input/output mask
13	DQ7	I/O	Data input/output	40	N.C/RFU	_	No connection (Reserved for future use)
14	VDD	_	Power supply	41	Vss	_	Ground
15	LDQM	ı	Data input/output mask	42	DQ8	I/O	Data input/output
16	WE	ı	Write enable input	43	VDDQ	_	Power supply for data output
17	CAS	ı	Column address strobe input	44	DQ9	I/O	Data input/output
18	RAS	I	Row address strobe input	45	DQ10	I/O	Data input/output
19	cs	ı	Chip select input	46	Vssq	-	Ground for data output
20	BA0	ı	Bank select address input	47	DQ11	I/O	Data input/output
21	BA1	I	Bank select address input	48	DQ12	I/O	Data input/output
22	A10/AP	I	Address input	49	VDDQ	_	Power supply for data output
23	A0	ı	Address input	50	DQ13	I/O	Data input/output
24	A1	ı	Address input	51	DQ14	I/O	Data input/output
25	A2	ı	Address input	52	Vssq	-	Ground for data output
26	A3	ı	Address input	53	DQ15	I/O	Data input/output
27	VDD	-	Power supply	54	Vss	ı	Ground

58

PDP-R06G

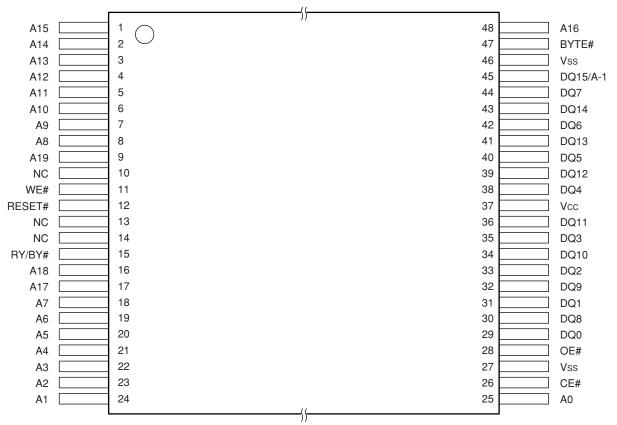
■ S29AL016D70TFI010 (MR MAIN ASSY : IC5404)

6

• 16M Flash Memory

5

• Pin Arrangement (Top view)



7

8

В

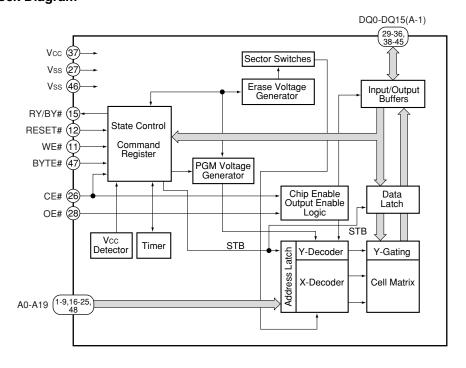
С

D

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Block Diagram

5



PDP-R06G

59

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F

2 = 3 = 4

Pin Function

No.	Pin Name	1/0	Pin Function	No.	Pin Name	1/0	Pin Function
1	A15	I	Address input	25	A0	1	Address input
2	A14	I	Address input	26	CE#	ı	Chip enable
3	A13	I	Address input	27	Vss	_	Device ground
4	A12	I	Address input	28	OE#	ı	Output enable
5	A11	I	Address input	29	DQ0	I/O	Data input/output
6	A10	I	Address input	30	DQ8	I/O	Data input/output
7	A9	I	Address input	31	DQ1	I/O	Data input/output
8	A8	I	Address input	32	DQ9	I/O	Data input/output
9	A19	I	Address input	33	DQ2	I/O	Data input/output
10	NC	_	No connection	34	DQ10	I/O	Data input/output
11	WE#	I	Write enable	35	DQ3	I/O	Data input/output
12	RESET#	I	Hardware reset	36	DQ11	I/O	Data input/output
13	NC	_	No connection	37	Vcc	_	3V single power supply
14	NC	-	No connection	38	DQ4	I/O	Data input/output
15	RY/BY#	0	Ready/Busy output	39	DQ12	I/O	Data input/output
16	A18	I	Address input	40	DQ5	I/O	Data input/output
17	A17	I	Address input	41	DQ13	I/O	Data input/output
18	A7	I	Address input	42	DQ6	I/O	Data input/output
19	A6	- 1	Address input	43	DQ14	I/O	Data input/output
20	A5	I	Address input	44	DQ7	I/O	Data input/output
21	A4	1	Address input	45	DQ15/A-1	I/O	DQ15: Data input/output, word mode A-1: LSB address input, byte mode
22	A3	I	Address input	46	Vss	_	Device ground
23	A2	I	Address input	47	BYTE#	I	Selects 8-bit or 16-bit mode
24	A1	ı	Address input	48	A16		Address input

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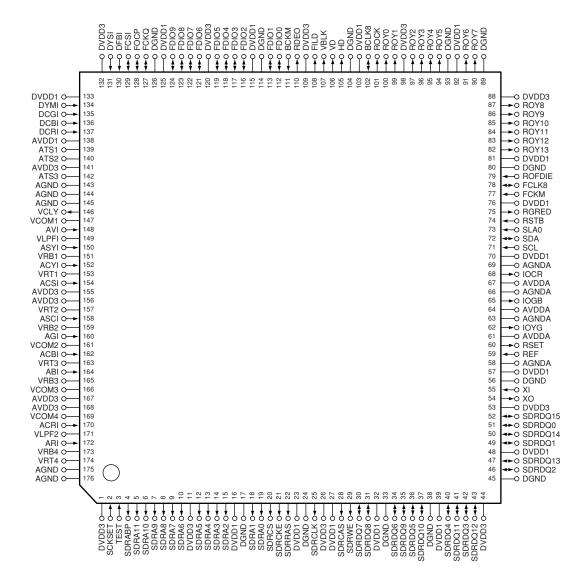
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PDP-R06G

■ 2 ■ 3 ■ 4

- Video Decoder (for main screen)
- Pin Arrangement (Top view)



61

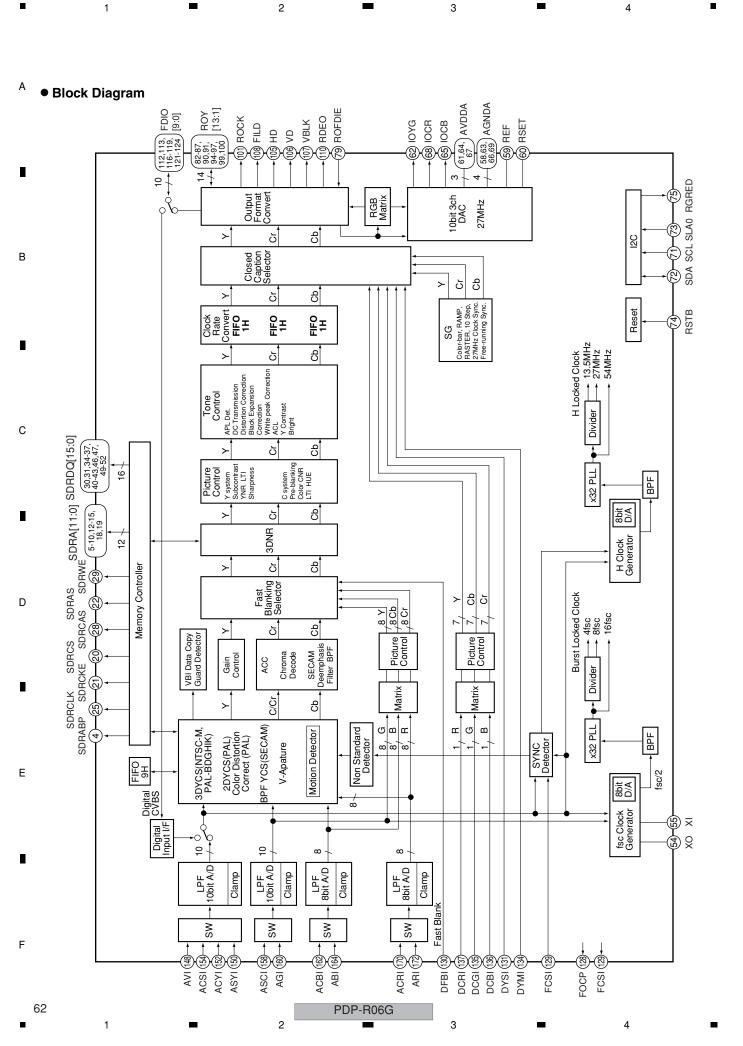
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Pin Function

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Pir	Pin Function						
No.	Pin Name	I/O	Pin Function				
1	DVDD3	_	Digital power supply (3.3V)				
2	SCKSET	- 1	Test mode selection (L: Normal, H: Test mode)				
3	TEST	- 1	Test setting (L: Normal, H: Test mode)				
4	SDRABP	0	All bank precharge output for external memory (Active High)				
5	SDRA11	0	Address output for external memory				
6	SDRA10	0	Address output for external memory				
7	SDRA9	0	Address output for external memory				
8	SDRA8	0	Address output for external memory				
9	SDRA7	0	Address output for external memory				
10	SDRA6	0	Address output for external memory				
11	DVDD3	_	Digital power supply (3.3V)				
12	SDRA5	0	Address output for external memory				
13	SDRA4	0	Address output for external memory				
14	SDRA3	0	Address output for external memory				
15	SDRA2	0	Address output for external memory				
16	DVDD1	_	Digital power supply (1.5V)				
17	DGND	<u> </u>	Digital ground				
18	SDRA1	0	Address output for external memory				
19	SDRA0	0	Address output for external memory				
20	SDRCS	0	Chip select output for external memory (Active Low)				
21	SDRCKE	0	Clock enable output for external memory (Active Low)				
22	SDRRAS	0	Row address strobe output for external memory (Active Low)				
23	DVDD1		Digital power supply (1.5V)				
24	DGND	-	Digital ground				
25	SDRCLK	0	Clock output for external memory				
26	DVDD3	-	·				
27	DVDD1		Digital power supply (3.3V)				
28	SDRCAS	0	Digital power supply (1.5V) Column address strobe output for external memory (Active Low)				
29	SDRWE	0					
30	SDRDQ7	1/0	Write enable output for external memory (Active Low) Data input/output for external memory				
31	SDRDQ7	1/0	Data input/output for external memory				
32	DVDD1	-	Digital power supply (1.5V)				
33	DGND		Digital ground				
34	SDRDQ6	I/O	Data input/output for external memory				
35	SDRDQ9	I/O	Data input/output for external memory				
36	SDRDQ5	I/O	Data input/output for external memory				
37	SDRDQ10	I/O	Data input/output for external memory				
38	DGND	-	Digital ground				
39	DVDD1	 	Digital power supply (1.5V)				
40	SDRDQ4	I/O	Data input/output for external memory				
41	SDRDQ11	I/O	Data input/output for external memory				
42	SDRDQ3	I/O	Data input/output for external memory				
43	SDRDQ12	I/O	Data input/output for external memory				
44	DVDD3	-	Digital power supply (3.3V)				
45	DGND	+	Digital ground				
46	SDRDQ2	I/O					
	SDRDQ2 SDRDQ13	1	Data input/output for external memory				
47	DVDD1	I/O	Data input/output for external memory Digital power supply (1.5V)				
48 49	SDRDQ1	I/O					
50 50	SDRDQ1 SDRDQ14	1/0	Data input/output for external memory				
50	אטחטטון 14	1/0	Data input/output for external memory				

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63

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PDP-R06G 7

No.	Pin Name	I/O	Pin Function		
51	SDRDQ0	I/O	Data input/output for external memory		
52	SDRDQ15	I/O	Data input/output for external memory		
53	DVDD3	_	Digital power supply (3.3V)		
54	XO	0	Reference clock output Connect a 24.576MHz crystal.		
55	XI	I	Reference clock input Connect a 24.576MHz crystal.		
56	DGND	_	Digital ground		
57	DVDD1	_	Digital power supply (1.5V)		
58	AGNDA	_	Analog ground for DAC		
59	REF	I	External reference input		
60	RSET	0	Connect a 620 ohm resistor for external adjustment to AGND		
61	AVDDA	_	Analog power supply for DAC (3.3V)		
62	IOYG	0	Color-difference component Y / RGB component G output signal		
63	AGNDA	<u> </u>	Analog ground for DAC		
64	AVDDA	_	Analog power supply for DAC (3.3V)		
65	IOGB	0	Color-difference component Cb / RGB component B output signal		
66	AGNDA	 -	Analog ground for DAC		
67	AVDDA	+	Analog power supply for DAC (3.3V)		
68	IOCR	0	Color-difference component Cr / RGB component R output signal		
69	AGNDA	+	Analog ground for DAC		
	DVDD1	-			
70		- 	Digital power supply (1.5V)		
71	SCL		I2C bus clock input Connect to SCL line of the system.		
72	SLA0	1/0	I2C bus data input/output Connect to SDA line of the system.		
73	RSTB		I2C bus slave address select input (L: B8h/B9h, H: BAh/BBh)		
74	RGRED	1	System reset input (Active Low)		
75		0	I2C register read flag output (Active Low)		
76	DVDD1	-	Digital power supply (1.5V)		
77	FCKM	1/0	FCLK8 test mode selection (L: Normal, H: Test mode)		
78	FCLK8	I/O	Line-lock clock monitor input/output		
79	ROFDIE		Output enable of the video input/output terminal L: Output terminal Hi-Z, H: Output enable		
80	DGND	-	Digital ground		
81	DVDD1	 -	Digital power supply (1.5V)		
82	ROY13	0	Digital ITU-R BT. 656/component output Digital RGB component (8 bit) output		
83	ROY12	0	Digital ITU-R BT. 656/component output Digital RGB component (8 bit) output		
84	ROY11	0	Digital ITU-R BT. 656/component output Digital RGB component (8 bit) output		
85	ROY10	0	Digital ITU-R BT. 656/component output Digital RGB component (8 bit) output		
86	ROY9	0	Digital ITU-R BT. 656/component output Digital RGB component (8 bit) output		
87	ROY8	0	Digital ITU-R BT. 656/component output Digital RGB component (8 bit) output		
88	DVDD3		Digital power supply (3.3V)		
89	DGND	-	Digital ground		
90	ROY7	0	Digital ITU-R BT. 656/component output Digital RGB component (8 bit) output		
91	ROY6	0	Digital ITU-R BT. 656/component output Digital RGB component (8 bit) output		
92	DVDD1		Digital power supply (1.5V)		
93	DGND	 -	Digital ground		
94	ROY5	0	Digital ITU-R BT. 656/component output Digital RGB component (8 bit) output		
95	ROY4	0	Digital ITU-R BT. 656/component output Digital RGB component (8 bit) output		
96	ROY3	0	Digital ITU-R BT. 656/component output Digital RGB component (8 bit) output		
97	ROY2	0	Digital ITU-R BT. 656/component output Digital RGB component (8 bit) output		
98	DVDD3	-	Digital power supply (3.3V)		
99	ROY1	0	Digital ITU-R BT. 656/component output Digital RGB component (8 bit) output		

PDP-R06G

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100 ROY0

Digital ITU-R BT. 656/component output Digital RGB component (8 bit) output

No.	Pin Name	I/O	Pin Function	
101	ROCK	0	Clock for digital ITU-R BT. 656/component output	
102	BCLK8	I/O	Line-lock clock monitor input/output	
103	DVDD1	_	Digital power supply (1.5V)	
104	DGND	_	Digital ground	
105	HD	0	Horizontal sync. signal output	
106	VD	0	Vertical sync. signal output	
107	VBLK	0	V blanking output	
108	FILD	0	Field output	
109	DVDD3	_	Digital power supply (3.3V)	
110	RDEO	0	Effective pixel area output	
111	BCKM	I	Test mode selection of BCLK8 pin (L: Normal, H: Test mode)	
112	FDIO0	I/O	Digital 8/10 bit Cb, Cr output / Input at UPD64031A digital connection Open at no use.	
113	FDIO1	I/O	Digital 8/10 bit Cb, Cr output / Input at UPD64031A digital connection Open at no use.	
114	DGND	_	Digital ground	
115	DVDD1	-	Digital power supply (1.5V)	
116	FDIO2	I/O	Digital 8/10 bit Cb, Cr output / Input at UPD64031A digital connection Open at no use.	
117	FDIO3	I/O	Digital 8/10 bit Cb, Cr output / Input at UPD64031A digital connection Open at no use.	
	FDIO4	I/O	Digital 8/10 bit Cb, Cr output / Input at UPD64031A digital connection Open at no use.	
	FDIO5	I/O	Digital 8/10 bit Cb, Cr output / Input at UPD64031A digital connection Open at no use.	
	DVDD3	-	Digital power supply (3.3V)	
	FDIO6	I/O	Digital 8/10 bit Cb, Cr output / Input at UPD64031A digital connection Open at no use.	
	FDIO7	I/O	Digital 8/10 bit Cb, Cr output / Input at UPD64031A digital connection Open at no use.	
	FDIO8	I/O	Digital 8/10 bit Cb, Cr output / Input at UPD64031A digital connection Open at no use.	
124	FDIO9	I/O	Digital 8/10 bit Cb, Cr output / Input at UPD64031A digital connection Open at no use.	
	DVDD1	_	Digital power supply (1.5V)	
126	DGND	_	Digital ground	
127	FCKQ	I/O	Sampling clock output for digital connection	
128	FOCP	I/O	Clamp pulse output for digital connection / Timing output for digital RGB input (VD)	
129	FCSI	I/O	Sync sep. signal input / Timing output for RGB input (HD)	
130	DFBI	- 1	Fast blanking signal input for analog RGB input	
131	DYSI	ı	YS signal input for digital RGB input	
132	DVDD3	_	Digital power supply (3.3V)	
133	DVDD1	_	Digital power supply (1.5V)	
134	DYMI	1	YM signal input for digital RGB input	
135	DCGI	I	Digital RGB/G signal input	
136	DCBI	I	Digital RGB/B signal input	
137	DCRI	I	Digital RGB/R signal input	
138	AVDD1	_	Analog power supply (1.5V)	
139	ATS1	_	Analog test input Normally, connect to GND.	
140	ATS2	_	Analog test input Normally, connect to GND.	
141	AVDD3	_	Analog power supply (3.3V)	
142	ATS3	_	Analog test input Normally, connect to GND.	
143	AGND	_	Analog ground	
144	AGND	_	Analog ground	
145	AGND	_	Analog ground	
146	VCLY	0	ADC1 clamp voltage	
	VCOM1	_	ADC1 common-mode reference voltage	
148	AVI	- 1	ADC1 composite/Y signal input	
149	VLPFI	_	Analog test output Connect to GND via 0.1µF capacitor.	
150	ASYI	1	ADC1 composite/Y signal input	

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PDP-R06G

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No.	Pin Name	I/O	Pin Function	
151	VRB1	_	ADC1 bottom reference voltage	
152	ACYI	ı	ADC1 composite/Y signal input	
153	VRT1	_	ADC1 top reference voltage	
154	ACSI	ı	ADC1 composite/Y signal input	
155	AVDD3	_	Analog power supply for ADC (3.3V)	
156	AVDD3	_	Analog power supply for ADC (3.3V)	
157	VRT2	_	ADC2 top reference voltage	
158	ASCI	ı	ADC2 separate C signal input	
159	VRB2	_	ADC2 bottom reference voltage	
160	AGI	- 1	ADC2 RGB component G signal input	
161	VCOM2	_	ADC2 common-mode reference voltage	
162	ACBI	1	ADC3 color-difference component Cb signal input	
163	VRT3	_	ADC3 top reference voltage	
164	ABI	ı	ADC3 RGB component B signal input	
165	VRB3	_	ADC3 bottom reference voltage	
166	VCOM3	_	ADC3 common-mode reference voltage	
167	AVDD3	_	Analog power supply for ADC (3.3V)	
168	AVDD3	_	Analog power supply for ADC (3.3V)	
169	VCOM4	_	ADC4 common-mode reference voltage	
170	ACRI	- 1	ADC4 color-difference component Cr signal input	
171	VLPF2	_	Analog test output	
172	ARI	- 1	ADC3 RGB component R signal input	
173	VRB4	-	ADC4 bottom reference voltage	
174	VRT4	-	ADC4 top reference voltage	
175	AGND	-	Analog ground	
176	AGND	_	Analog ground	

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PDP-R06G

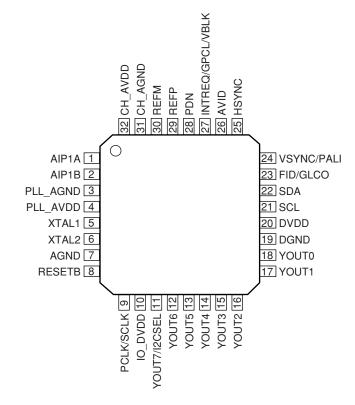
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■ TVP5150AM1PBS (MR MAIN ASSY : IC6001)

• Video Decoder (for Subscreen)

• Pin Arrangement (Top view)

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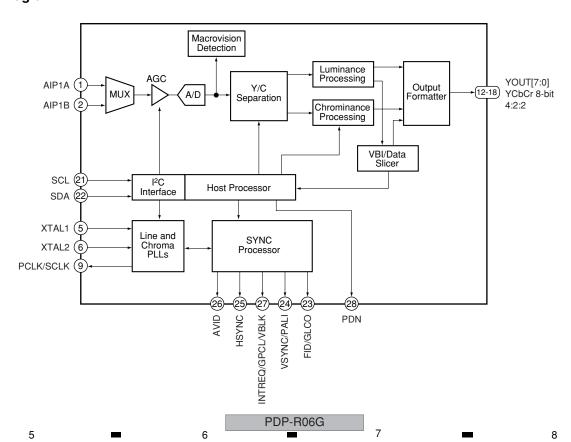
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Block Diagram



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• Pin Function

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No. Pin Name I/O		I/O	Pin Function				
1	AIP1A	ı	Analog input				
2	AIP1B	ı	Analog input				
3	PLL_AGND	1	PLL ground Connect to analog ground.				
4	PLL_AVDD	I	PLL power supply (1.8V)				
5	XTAL1	I	External clock reference				
6	XTAL2	0	External clock reference				
7	AGND	ı	Substrate Connect to analog ground.				
8	RESETB	ı	Active-low reset				
9	PCLK/SCLK	0	System clock at either 1x or 2x the frequency of the pixel clock				
10	IO_DVDD	I	Digital power supply (3.3V)				
11	YOUT(7)/I2CSEL	I/O	I2CSEL: Determines address for I ² C (sampled during reset) YOUT7: MSB of output decoded ITU-R BT.656 output/YCbCr 4:2:2 output				
12	YOUT6	I/O	Output decoded ITU-R BT.656 output/YCbCr 4:2:2 output with discrete sync				
13	YOUT5	I/O	Output decoded ITU-R BT.656 output/YCbCr 4:2:2 output with discrete sync				
14	YOUT4	I/O	Output decoded ITU-R BT.656 output/YCbCr 4:2:2 output with discrete sync				
15	YOUT3	I/O	Output decoded ITU-R BT.656 output/YCbCr 4:2:2 output with discrete sync				
16	YOUT2	I/O	Output decoded ITU-R BT.656 output/YCbCr 4:2:2 output with discrete sync				
17	YOUT1	I/O	Output decoded ITU-R BT.656 output/YCbCr 4:2:2 output with discrete sync				
18	YOUT0	I/O Output decoded ITU-R BT.656 output/YCbCr 4:2:2 output with discrete sync					
19	DGND	ı	Digital ground				
20	DVDD	ı	Digital power supply (1.8V)				
21	SCL	I/O	I ² C serial clock (open drain)				
22	SDA	I/O	I ² C serial data (open drain)				
23	FID/GLCO	0	FID: Odd/even field indicator or vertical lock indicator GLCO: This serial output carries color PLL information				
24	VSYNC/PALI	0	VSYNC: Vertical synchronization signal PALI: PAL line indicator or horizontal lock indicator				
25	HSYNC	0	Horizontal synchronization signal				
26	AVID	0	Active video indicator				
27	INTREQ/GPCL /VBLK	I/O	INTREQ: Interrupt request output GPCL: General-purpose control logic				
28	PDN I Power-down terminal (active low)						
29	REFP	ı	A/D reference supply				
30	REFM	ı	A/D reference ground				
31	CH_AGND	ı	Analog ground				
32	CH_AVDD	I	Analog power supply (1.8V)				

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68

PDP-R06G

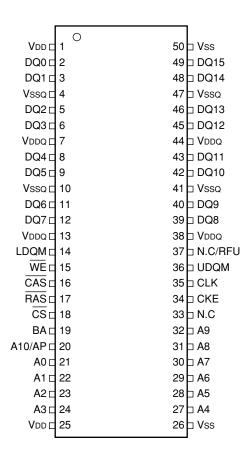
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■ K4S161622H-TC60 (MR MAIN ASSY : IC6002)

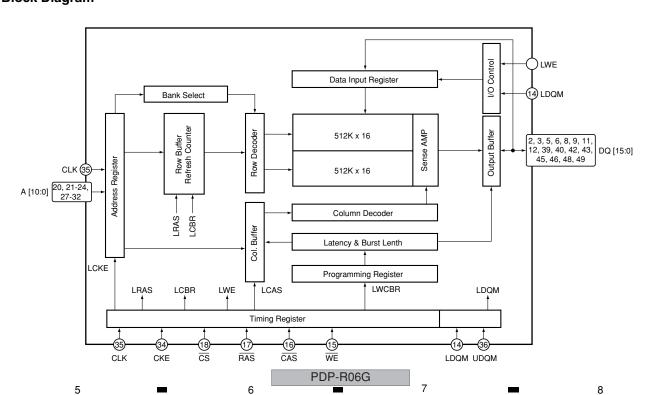
• 16M SDRAM (for Main VDEC)

• Pin Arrangement (Top view)

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Block Diagram



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• Pin Function

No.	Pin Name	I/O	Pin Function	No.	Pin Name	I/O	Pin Function
1	VDD	-	Power supply	26	Vss	_	Ground
2	DQ0	I/O	Data input / output	27	A4	I	Address input
3	DQ1	I/O	Data input / output	28	A5	I	Address input
4	Vssq	ı	Ground for data output	29	A6	1	Address input
5	DQ2	I/O	Data input / output	30	A7	1	Address input
6	DQ3	1/0	Data input / output	31	A8	_	Address input
7	VDDQ	-	Power supply for data output	32	A9	1	Address input
8	DQ4	I/O	Data input / output	33	N.C	-	No connection
9	DQ5	I/O	Data input / output	34	CKE	I	Clock enable input
10	Vssq	-	Ground for data output	35	CLK	I	System clock input
11	DQ6	I/O	Data input / output	36	UDQM	I	Data input / output mask input
12	DQ7	I/O	Data input / output	37	N.C/RFU	-	No connection / Reserved for future use
13	VDDQ	ı	Power supply for data output	38	VDDQ	ı	Power supply for data output
14	LDQM		Data input / output mask input	39	DQ8	I/O	Data input / output
15	WE		Write enable input	40	DQ9	I/O	Data input / output
16	CAS	I	Column address strobe input	41	Vssq	-	Ground for data output
17	RAS	- 1	Row address strobe input	42	DQ10	I/O	Data input / output
18	CS	- 1	Chip select input	43	DQ11	I/O	Data input / output
19	ВА		Bank select address input	44	VDDQ	ı	Power supply for data output
20	A10/AP	_	Address input	45	DQ12	1/0	Data input / output
21	A0	I	Address input	46	DQ13	I/O	Data input / output
22	A1	1	Address input	47	Vssq	ı	Ground for data output
23	A2	I	Address input	48	DQ14	I/O	Data input / output
24	A3	I	Address input	49	DQ15	I/O	Data input / output
25	VDD	-	Power supply	50	Vss	_	Ground

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70

PDP-R06G

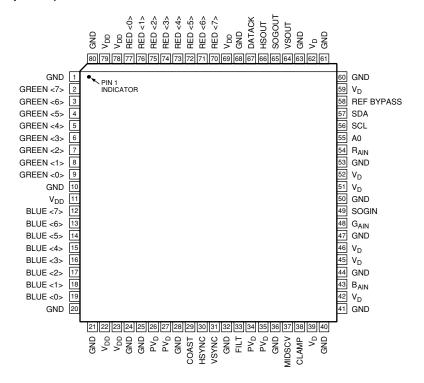
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■ AD9985KSTZ-110 (MR MAIN ASSY : IC6201)

• ADC

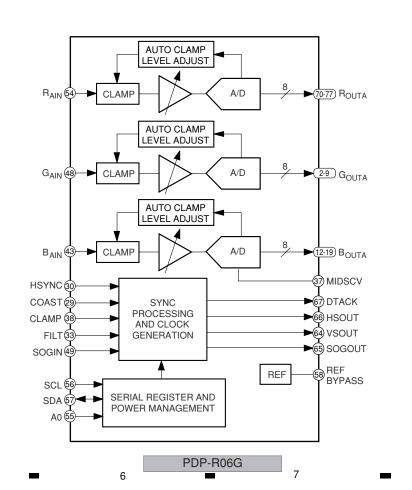
• Pin Arrangement (Top view)

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Block Diagram

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71

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Pin Function

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Pin Type	No.	PIN Name	Pin Function			
	54	Rain	Analog input for converter R			
	48	GAIN	Analog input for converter G			
	43	BAIN	Analog input for converter B			
Laureta	30	HSYNC	Horizontal sync input			
Inputs	31	VSYNC	Vertical sync input			
	49	SOGIN	Input for sync-on green			
	38	CLAMP	Clamp input (External CLAMP signal)			
	29	COAST	PLL COAST signal input			
	70-77	Red [7:0]	Outputs of converter red, bit 7 is the MSB			
	2-9	Green [7 : 0]	Outputs of converter green, bit 7 is the MSB			
	12-19	Blue [7 : 0]	Outputs of converter blue, bit 7 is the MSB			
Outputs	67	DATACK	Data output clock			
	66	HSOUT	HSYNC output (Phase-aligned with DATACK)			
	64	VSOUT	VSYNC output (Phase-aligned with DATACK)			
	65	SOGOUT	Sync-on-green slicer output			
	58	REF BYPASS	Internal reference bypass			
Reference	37	MIDSCV	Internal midscale voltage bypass			
	33	FILT	Connection for external filter components for internal PLL			
	39, 42, 45, 46, 51, 52, 59, 62	VD	Analog power supply			
	11, 22, 23, 69, 78, 79	VDD	Output power supply			
Power Supply	26, 27, 34, 35	PVD	PLL power supply			
	1, 10, 20, 21, 24, 25, 28, 32, 36, 40, 41, 44, 47, 50, 53, 60, 61, 63 68, 80	GND	Ground			
	57	SDA	Serial port data I/O			
Control	56	SCL	Serial port data clock (100 kHz maximum)			
	55	A0	Serial port address input 1			

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PDP-R06G

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■ SII9021CTU (MR MAIN ASSY: IC6404)

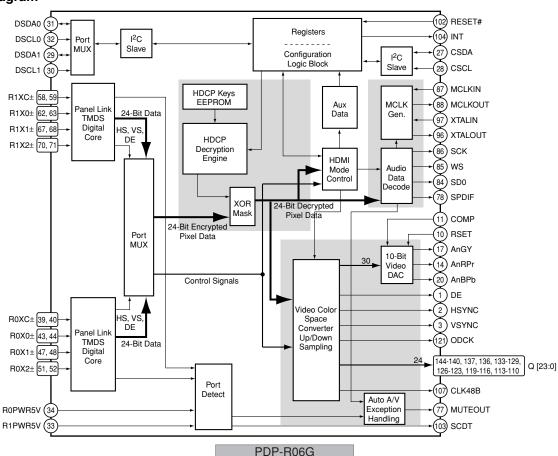
• HDMI Rx

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Pin Arrangement (Top view) DGND [DGND 36 DGND L
DVCC18 [
IOGND [
IOVCC [
MUTEOUT [
SPDIF
CVCC18 [
CGND [
RSVD [
RSVD [
RSVD [DGND
DVCC18
DVCC18
R0PWR5V
R1PWR5V
DSCL0
DSDA0
DSCL1
DSCL1
DSCL1
DSCL1
DSCL1
DSCL 35 34 33 32 31 79 80 81 82 30 29 28 27 RSVD [SD0 [WS [IOVCC IOGND CGND 83 84 85 86 87 26 25 24 23 22 21 20 19 18 17 SCK I DACDGND DACDGND
AnBPb
DACVCCB
DACGNDB
AnGY
DACVCCG
DACGNDG 16 15 14 13 12 11 10 9 8 7 6 5 4 3 DACGNDG
DACGNDG
DACYCCR
DACGNDR
COMP
DESET
DACAGND
DACAGND
DACAGND
DACAGND
DACAVCC 100 101 103 I DACOVI I IOVCC I IOGND I VSYNC I HSYNC INT [CVCC18 [CGND [104 105 106 CLK48B I

Block Diagram

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73

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• Pin Function

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Γ		Din Name	I/O	Din Function
╌	No.	Pin Name		Pin Function Data enable
╌	1	DE	0	
╌	2	HSYNC	0	Horizontal sync output control signal
╌	3	VSYNC	0	Vertical sync output control signal
╌	4	IOGND	 -	Input / output pin ground
╌	5	IOVCC	 -	Input / output pin VCC
-	6	DACOVCC		DAC output VCC
	7	DACAVCC	-	DAC analog VCC
-	8	DACAGND	<u> </u>	DAC analog ground
L	9	VREF	<u> </u>	-
L	10	RSET	<u> </u>	Full scale adjust resistor
L	11	COMP		Compensation
L	12	DACGNDR	_	DAC red ground
L	13	DACVCCR	_	DAC red VDD
	14	AnRPr	0	Analog video red, Pr output
	15	DACGNDG	_	DAC green ground
	16	DACVCCG	_	DAC green VDD
	17	AnGY	0	Analog video green, Y output
	18	DACGNDB	_	DAC blue ground
	19	DACVCCB	_	DAC blue VDD
ı	20	AnBPb	0	Analog video blue, Pb output
ı	21	DACDGND	_	DAC digital ground
ı	22	DACDVCC18		DAC digital VCC
ı	23	CVCC18	<u> </u>	Digital logic VCC
ı	24	CGND	+ -	Digital logic ground
ı	25	IOGND	<u> </u>	Input / output pin ground
ı	26	IOVCC	 	Input / output pin VCC
ŀ	27	CSDA	I/O	Configuration I ² C data
╁	28	CSCL	1	Configuration I ² C clock
╁	29	DSDA1	1/0	DDC I ² C data for port 1
╁	30	DSCL1	1	DDC I ² C clock for port 1
╁	31	DSDA0	1/0	DDC I ² C data for port 0
╌	32	DSCL0	I/O	DDC I ² C clock for port 0
╁	33	R1PWR5V	1	Port 1 transmitter detect
╌	34	R0PWR5V	+ ;	
╌		 	+ '	Port 0 transmitter detect
╌	35	DVCC18	_	ACR PLL digital VCC
╌	36	DGND	+ -	ACR PLL ground
╌	37	PVCC0	+ -	TMDS port 0 PLL VCC
╌	38	AVCC	+-	TMDS analog VCC
╌	39	R0XC-	+ !	TMDS input clock
-	40	R0XC+		TMDS input clock
-	41	AGND		TMDS analog ground
╌	42	AVCC		TMDS analog VCC
-	43	R0X0-	I	TMDS input data
	44	R0X0+	I	TMDS input data
	45	AGND	<u> </u>	TMDS analog ground
L	46	AVCC		TMDS analog VCC
L	47	R0X1-	I	TMDS input data
	48	R0X1+	I	TMDS input data
	49	AGND	_	TMDS analog ground
L	50	AVCC		TMDS analog VCC

74

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No.	Pin Name	I/O	Pin Function	
51	R0X2-	1	TMDS input data	
52	R0X2+		TMDS input data	
53	AGND	<u> </u>	TMDS analog ground	
54	TMDSPGND	_	TMDS PLL ground	
55	PVCC1	_	TMDS port 1 PLL VCC	
56	RSVD_A	_	Reserved pin	
57	AVCC	_	TMDS analog VCC	
58	R1XC-		TMDS input clock	
59	R1XC+	i	TMDS input clock	
60	AGND	-	TMDS analog ground	
61	AVCC	_	TMDS analog VCC	
62	R1X0-	 	TMDS input data	
63	R1X0+	<u> </u>	TMDS input data	
			TMDS input data TMDS analog ground	
64 65	AGND AVCC		TMDS analog VCC	
		 -		
66	R1X1-		TMDS input data	
67	R1X1+	I	TMDS input data	
68	AGND		TMDS analog ground	
69	AVCC		TMDS analog VCC	
70	R1X2-	l l	TMDS input data	
71	R1X2+	I	TMDS input data	
72	AGND		TMDS analog ground	
73	DGND		ACR PLL ground	
74	DVCC18	_	ACR PLL digital VCC	
75	IOGND	_	Input / output pin ground	
76	IOVCC	_	Input / output pin VCC	
77	MUTEOUT	0	Mute audio output	
78	SPDIF	0	S/PDIF audio output	
79	CVCC18	_	Digital logic VCC	
80	CGND	_	Digital logic ground	
81	RSVD	0	-	
82	RSVD	0	-	
83	RSVD	0	-	
84	SD0	0	I ² S serial data output	
85	WS	0	I ² S word select output	
86	SCK	0	I ² S serial clock output	
87	MCLKIN	I	Audio master clock input reference	
88	MCLKOUT	0	Audio master clock output	
89	IOVCC	_	Input / output pin VCC	
90	IOGND	_	Input / output pin ground	
91	CGND	_	Digital logic ground	
92	CVCC18	_	Digital logic VCC	
93	NC	_	No connection	
94	AUDPVCC18	_	ACR PLL VCC	
95	AUDPGND	_	ACR PLL ground	
96	XTALOUT	0	Crystal clock output	
97	XTALIN	1	Crystal clock input	
98	XTALVCC		ACR PLL crystal input VCC	
99	REGVCC	_	ACR PLL regulator VCC	
100	NC		No connection	
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No.	Pin Name	I/O	Pin Function
101	RSVDL	1	Reserved, must be tied LOW
102	RESET#	ı	Reset pin, active LOW
103	SCDT	0	Indicates active video at HDMI input port
104	INT	0	Interrupt output
105	CVCC18	-	Digital logic VCC
106	CGND	_	Digital logic ground
107	CLK48B	I/O	Data bus latch enable
108	IOGND	_	Input / output pin ground
109	IOVCC	_	Input / output pin VCC
110	Q23	0	24-bit output pixel data bus
111	Q22	0	24-bit output pixel data bus
112	Q21	0	24-bit output pixel data bus
113	Q20	0	24-bit output pixel data bus
114	CVCC18	_	Digital logic VCC
115	CGND	_	Digital logic ground
116	Q19	0	24-bit output pixel data bus
117	Q18	0	24-bit output pixel data bus
118	Q17	0	24-bit output pixel data bus
119	Q16	0	24-bit output pixel data bus
120	IOGND	_	Input / output pin ground
121	ODCK	0	Output data clock
122	IOVCC	_	Input / output pin VCC
123	Q15	0	24-bit output pixel data bus
124	Q14	0	24-bit output pixel data bus
125	Q13	0	24-bit output pixel data bus
126	Q12	0	24-bit output pixel data bus
127	CGND	_	Digital logic ground
128	CVCC18	_	Digital logic VCC
129	Q11	0	24-bit output pixel data bus
130	Q10	0	24-bit output pixel data bus
131	Q9	0	24-bit output pixel data bus
132	Q8	0	24-bit output pixel data bus
133	Q7	0	24-bit output pixel data bus
134	IOVCC	_	Input / output pin VCC
135	IOGND	_	Input / output pin ground
136	Q6	0	24-bit output pixel data bus
137	Q5	0	24-bit output pixel data bus
138	CGND	_	Digital logic ground
139	CVCC18	_	Digital logic VCC
140	Q4	0	24-bit output pixel data bus
141	Q3	0	24-bit output pixel data bus
142	Q2	0	24-bit output pixel data bus
143	Q1	0	24-bit output pixel data bus
144	Q0	0	24-bit output pixel data bus

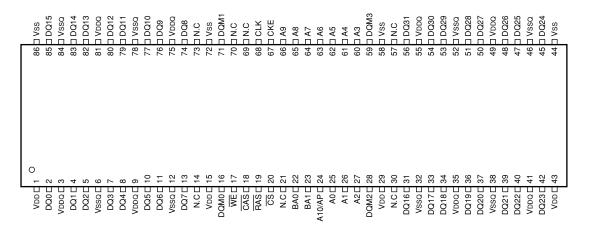
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■ K4S643232H-TC60 (MR MAIN ASSY : IC6801, IC6802)

• 64M SDRAM (for Silvia)

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Pin Arrangement (Top view)



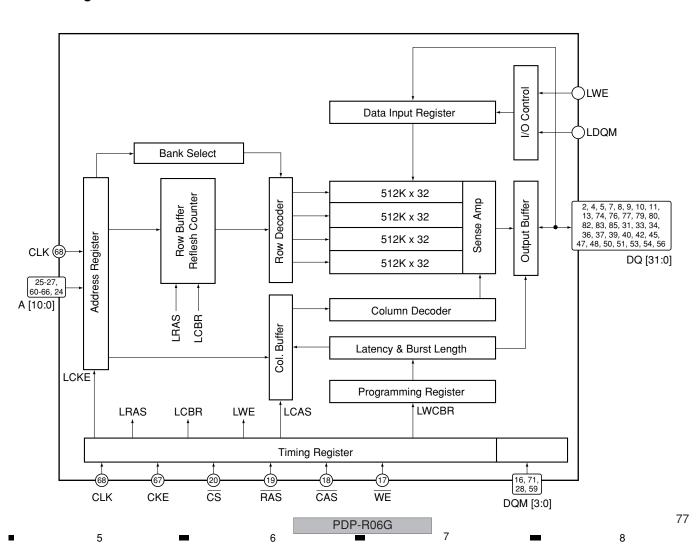
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Block Diagram



• Pin Function

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	No.	Pin Name	I/O	Pin Function	No.	Pin Name	I/O	Pin Function
	1	VDD	ı	Power supply	44	Vss	-	Ground
ſ	2	DQ0	I/O	Data input / output	45	DQ24	I/O	Data input / output
Γ	3	VDDQ	-	Power supply for data output	46	Vssq	-	Ground for data output
ſ	4	DQ1	I/O	Data input / output	47	DQ25	I/O	Data input / output
Γ	5	DQ2	I/O	Data input / output	48	DQ26	I/O	Data input / output
ſ	6	Vssq	-	Ground for data output	49	VDDQ	-	Power supply for data output
	7	DQ3	1/0	Data input / output	50	DQ27	I/O	Data input / output
	8	DQ4	0	Data input / output	51	DQ28	I/O	Data input / output
	9	VDDQ	ı	Power supply for data output	52	Vssq	-	Ground for data output
	10	DQ5	1/0	Data input / output	53	DQ29	I/O	Data input / output
L	11	DQ6	I/O	Data input / output	54	DQ30	I/O	Data input / output
L	12	Vssq	ı	Ground for data output	55	VDDQ	-	Power supply for data output
	13	DQ7	I/O	Data input / output	56	DQ31	I/O	Data input / output
	14	N.C	-	No connection	57	N.C	_	No connection
	15	VDD	-	Power supply	58	Vss	_	Ground
	16	DQM0	I	Data input / output mask input	59	DQM3	l	Data input / output mask input
	17	WE	I	Write enable input	60	A3	l	Address input
	18	CAS	I	Column address strobe input	61	A4	l	Address input
	19	RAS	- 1	Row address strobe input	62	A5	I	Address input
	20	CS	ı	Chip select input	63	A6	I	Address input
	21	N.C	-	No connection	64	A7	I	Address input
	22	BA0	ı	Bank select address input	65	A8	I	Address input
L	23	BA1	ļ	Bank select address input	66	A9	I	Address input
	24	A10/AP	ı	Address input	67	CKE	I	Clock enable input
L	25	A0	ļ	Address input	68	CLK	I	System clock input
L	26	A1	I	Address input	69	N.C	_	No connection
	27	A2	I	Address input	70	N.C	-	No connection
L	28	DQM2	I	Data input / output mask input	71	DQM1	ı	Data input / output mask input
L	29	VDD	_	Power supply	72	Vss	_	Ground
L	30	N.C	_	No connection	73	N.C	_	No connection
L	31	DQ16	I/O	Data input / output	74	DQ8	I/O	Data input / output
ļ	32	Vssq	_	Ground for data output	75	VDDQ	_	Power supply for data output
L	33	DQ17	I/O	Data input / output	76	DQ9	I/O	Data input / output
ļ	34	DQ18	I/O	Data input / output	77	DQ10	I/O	Data input / output
ŀ	35	VDDQ	-	Power supply for data output	78	Vssq	-	Ground for data output
ŀ	36	DQ19	I/O	Data input / output	79	DQ11	I/O	Data input / output
ŀ	37	DQ20	I/O	Data input / output	80	DQ12	I/O	Data input / output
ļ	38	Vssq	-	Ground for data output	81	VDDQ	-	Power supply for data output
-	39	DQ21	I/O	Data input / output	82	DQ13	I/O	Data input / output
-	40	DQ22	I/O	Data input / output	83	DQ14	I/O	Data input / output
-	41	VDDQ	_	Power supply for data output	84	Vssq	_	Ground for data output
-	42	DQ23	I/O	Data input / output	85	DQ15	I/O	Data input / output
L	43	VDD	_	Power supply	86	Vss		Ground

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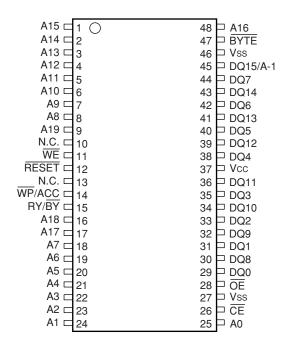
78

■ MBM29DL162TE70TN (MR MAIN ASSY : IC5207, IC7002)

16M Flash for Carrera MANTA

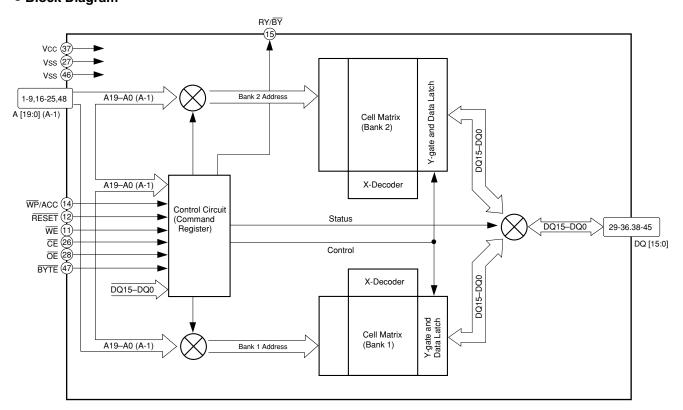
• Pin Arrangement (Top view)

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Block Diagram

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79

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PDP-R06G

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Pin Function

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No.	Pin Name	I/O	Pin Function	
1	A15	I	Address input	
2	A14	1	Address input	
3	A13	I	Address input	
4	A12	I	Address input	
5	A11	1	Address input	
6	A10	I	Address input	
7	A9	I	Address input	
8	A8	I	Address input	
9	A19	I	Address input	
10	N.C.	I	No connection	
11	WE	I	Write enable input	
12	RESET	I	Hardware reset	
13	N.C.	-	No connection	
14	WP/ACC	I	Hardware write protect / Acceleration	
15	RY/BY	0	Ready / Busy output	
16	A18	I	Address input	
17	A17	I	Address input	
18	A7	I	Address input	
19	A6	I	Address input	
20	A5	I	Address input	
21	A4	I	Address input	
22	A3	I	Address input	
23	A2	ı	Address input	
24	A1	ı	Address input	
25	A0	ı	Address input	
26	CE	ı	Chip enable input	
27	Vss	_	Ground	
28	ŌĒ	I	Output enable input	
29	DQ0	I/O	Data input / output	
30	DQ8	I/O	Data input / output	
31	DQ1	I/O	Data input / output	
32	DQ9	I/O	Data input / output	
33	DQ2	I/O	Data input / output	
34	DQ10	I/O	Data input / output	
35	DQ3	I/O	Data input / output	
36	DQ11	I/O	Data input / output	
37	Vcc	-	Power supply	
38	DQ4	I/O	Data input / output	
39	DQ12	I/O	Data input / output	
40	DQ5	I/O	Data input / output	
41	DQ13	I/O	Data input / output	
42	DQ6	I/O	Data input / output	
43	DQ14	I/O	Data input / output	
44	DQ7	I/O	Data input / output	
45	DQ15/A-1	I/O	Data input / output / Address input	
46	Vss	_	Ground	
47	BYTE	ı	Selects 8-bit or 16-bit mode	
48	A16	I	Address input	

■ SII170BCLG64 (MR MAIN ASSY : IC7202)

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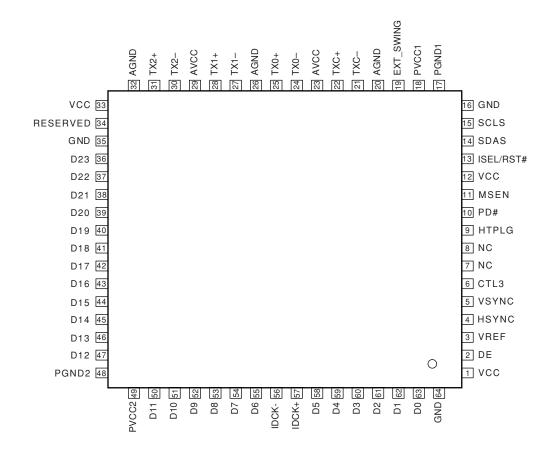
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• DVI Tx

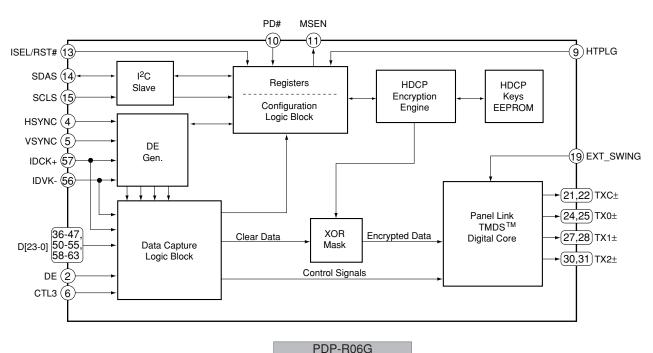
Pin Arrangement (Top view)

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Block Diagram

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81

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Pin Function

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	No.	Pin Name	I/O	Pin Function			
	1	VCC	_	Digital power supply (3.3V)			
	2	DE	I	Data enable			
	3	VREF	I	3.3V fixed			
	4	HSYNC	I	Horizontal sync. control signal input			
	5	VSYNC	I	Vertical sync. control signal input			
	6	CTL3	I	External CTL3 input			
	7	NC	_	No connection			
	8	NC	_	connection			
	9	HTPLG	I	Monitor chrage input			
	10	PD#	I	Power down input (Active low)			
	11	MSEN	0	Monitor sense output (open-collector output)			
	12	VCC	_	Digital power supply (3.3V)			
	13	ISEL/RST#	ı	I2C interface selecting input High: I2C interface is active			
	14	SDAS	I/O	DDC I2C data input/output			
	15	SCLS	ı	DDC I2C clock input			
	16	GND	_	Digital ground			
	17	PGND1	_	PLL analog ground			
	18	PVCC1	_	Analog power supply for PLL of primary side (3.3V)			
	19	EXT_SWING	I	Voltage regulation adjustment			
	20	AGND	-	Analog ground			
	21	TXC-	0	Differential signal clock output of TMDS Low voltage			
	22	TXC+	0	Differential signal clock output of TMDS Low voltage			
	23	AVCC	-	nalog power supply (3.3V)			
	24	TX0-	0	Differential signal clock output of TMDS Low voltage			
	25	TX0+	0	Differential signal clock output of TMDS Low voltage			
	26	AGND	-	Analog ground			
	27	TX1-	0	Differential signal clock output of TMDS Low voltage			
	28	TX1+	0	Differential signal clock output of TMDS Low voltage			
	29	AVCC	_	Analog power supply (3.3V)			
	30	TX2-	0	Differential signal clock output of TMDS Low voltage			
	31	TX2+	0	Differential signal clock output of TMDS Low voltage			
	32	AGND	-	Analog ground			
	33	VCC	_	Digital power supply (3.3V)			
	34	RESERVED	I	Reserved pin for Silicon Image Normally, fixed to low.			
	35	GND	_	Digital ground			
	36	D23	I	24-bit pixel bus input			
	37	D22	I	24-bit pixel bus input			
	38	D21	I	24-bit pixel bus input			
	39	D20	I	24-bit pixel bus input			
	40	D19	I	24-bit pixel bus input			

F

82

No.	Pin Name	I/O	Pin Function			
	D18					
41		I	24-bit pixel bus input			
42	D17		4-bit pixel bus input			
43	D16	I	24-bit pixel bus input			
44	D15	I	24-bit pixel bus input			
45	D14	I	24-bit pixel bus input			
46	D13	I	24-bit pixel bus input			
47	D12	1	24-bit pixel bus input			
48	PGND2	_	PLL analog ground			
49	PVCC2	_	Analog power supply for filter PLL (3.3V)			
50	D11	I	24-bit / 12-bit pixel bus input			
51	D10	I	24-bit / 12-bit pixel bus input			
52	D9	I	24-bit / 12-bit pixel bus input			
53	D8	I	24-bit / 12-bit pixel bus input			
54	D7	1	24-bit / 12-bit pixel bus input			
55	D6	1	24-bit / 12-bit pixel bus input			
56	IDCK-	I	Data clock - input			
57	IDCK+	- 1	Data clock + input			
58	D5	I	24-bit / 12-bit pixel bus input			
59	D4	I	24-bit / 12-bit pixel bus input			
60	D3	- 1	24-bit / 12-bit pixel bus input			
61	D2	I	24-bit / 12-bit pixel bus input			
62	D1	I	24-bit / 12-bit pixel bus input			
63	D0	I	24-bit / 12-bit pixel bus input			
64	GND	_	Digital ground			

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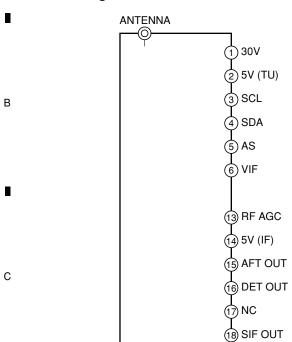
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■ 2 ■ 3 ■ 4

■ AXF1150 (MR MAIN ASSY : U4401)

• Front End

• Pin Arrangement



Pin Function

No.	Pin Name	Pin Function
1	30V	Power supply for 30V
2	5V (TU)	Power supply for tuner
3	SCL	
4	SDA	Terminal for I2C bus control
5	AS	
6	VIF	VIF output
13	RF AGC	RF AGC terminal
14	5V (IF)	Power supply for IF
15	AFT OUT	Analog AFT output
16	DET OUT	VIDEO output (Typical = 1.0Vp-p)
17	NC	No connection
18	SIF OUT	SIF output

84

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PDP-R06G

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■ AXY1117 (MR MAIN ASSY: U4201)

• 3 Outputs DD Control Unit

• Pin Arrangement

Vin	☐ 1 ☐ 14 ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐	Vo2
Vin	☐ 2 13 ☐	Vo2
GND	□ 3	
GND	☐ 4 12 ☐	GND
ON/OFF	□ 5	
GND	☐ 6 11 ☐	GND
	10 🗌	GND
	9 🗌	Vo1
Vo3	□ 7 8 □	Vo1

Pin Function

5

No.	Pin Name	Pin Function
1	Vin	land
2	Vin	Input
3	GND	Ourself for least side
4	GND	Ground for input side
5	ON/OFF	Output ON/OFF
6	GND	Ground for output side
7	Vo3	1.8V output
8	Vo1	3.3V output
9	Vo1	3.3V output
10	GND	
11	GND	Ground for output side
12	GND	
13	Vo2	1.2V output
14	Vo2	1.2V output

85

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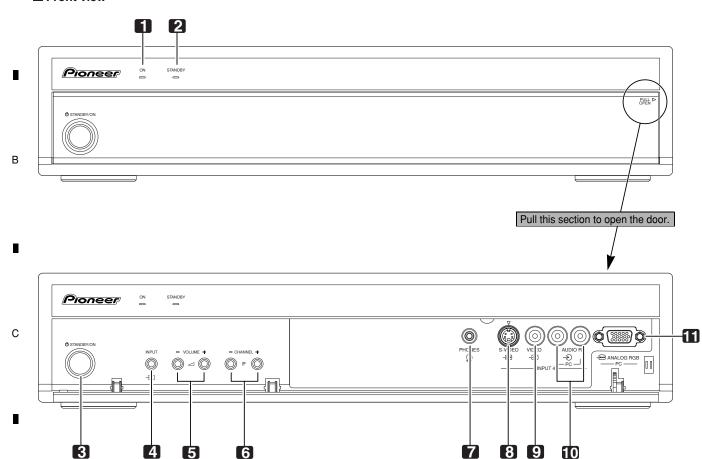
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PDP-R06G

8. PANEL FACILITIES

Front view



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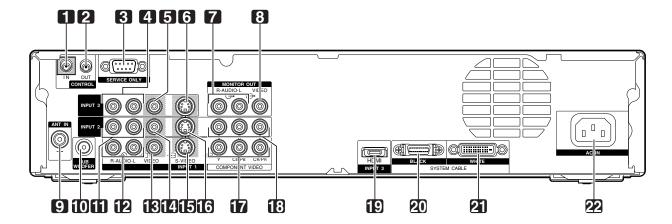
- 1 POWER ON indicator
 - 2 STANDBY indicator
 - 3 STANDBY/ON button
 - 4 **INPUT** button
- 5 VOLUME +/- buttons
 - 6 CHANNEL +/- buttons
 - 7 PHONES output terminal
 - 8 INPUT 4 terminal (S-VIDEO)
 - 9 INPUT 4 terminal (VIDEO)
 - **10** INPUT 4/PC INPUT terminals (AUDIO)
 - 11 PC INPUT terminal (ANALOG RGB)

86

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■ Rear view

5



- 1 CONTROL IN terminal
- 2 CONTROL OUT terminal
- **3** RS-232C terminal (used for factory setup)
- 4 INPUT 3 terminals (AUDIO)
- 5 INPUT 3 terminal (VIDEO)
- 6 INPUT 3 terminal (S-VIDEO)
- 7 MONITOR OUT terminals (AUDIO)
- 8 MONITOR OUT terminal (VIDEO)
- 9 ANT (Antenna) IN terminal
- 10 SUB WOOFER output terminal
- 11 INPUT 2 terminals (AUDIO)
- 12 INPUT 1 terminals (AUDIO)

5

- 13 INPUT 1 terminal (VIDEO)
- 14 INPUT 2 terminal (VIDEO)
- 15 INPUT 1 terminal (S-VIDEO)
- **16** INPUT 2 terminal (S-VIDEO)
- **17** INPUT 1 terminals
- (COMPONENT VIDEO: Y, CB/PB, CR/PR)
- **18** INPUT 2 terminals (COMPONENT VIDEO: Y, CB/PB, CR/PR)
- 19 INPUT 3 terminal (HDMI)
- 20 SYSTEM CABLE terminal (BLACK)
- 21 SYSTEM CABLE terminal (WHITE)
- 22 AC IN terminal

87

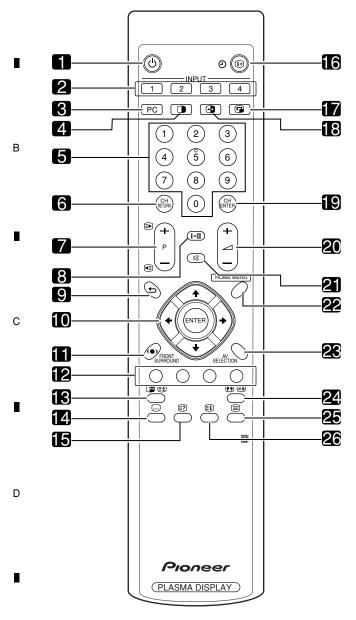
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■ Remote control unit



1 (^l)

Turns on the power to the Plasma Display or places it into standby mode.

2 INPUT 1, 2, 3, 4

Selects an input source of the Plasma Display. (INPUT1, INPUT2, INPUT3, INPUT4).

3 PC

Selects the PC terminal as an input source.

4

Switches the screen mode among 2-screen, picture-inpicture, and single-screen.

5 0-9

Switches on the power to the Plasma Display. TV/External input mode: Selects a channel.

TELETEXT mode: Selects a page.

6 CH RETURN

Returns to the previous channel.

7 P+/P-

TV/External input mode: Selects a channel.

3

TELETEXT mode: Selects a page.

8 I-II

Sets the sound multiplex mode.

9 **与** (RETURN)

Restores the previous menu screen.

10 **↑**/**↓**/**♦**/**→**

Selects a desired item on the setting screen.

ENTER

Executes a command.

11 FRONT SURROUND

Switches the Front Surround mode.

12 Color (RED/GREEN/YELLOW/BLUE)

TELETEXT mode: Selects a page.

13

Freezes a frame from a moving image. Press again to cancel the function.

TELETEXT mode: Stops updating Teletext pages. Press again to release the hold mode.

14

Jumps to the Teletext subtitle page.

15 €?

TELETEXT mode: Displays hidden characters.

16 (i

Displays the channel information.

17 🕝

Moves the location of the small screen when in the picture-in-picture mode.

18 🔂

Switches between the two screens when in the 2-screen or picture-in-picture mode.

19 CH ENTER

Executes a channel number.

20 🚄 +/🚄 -

Sets the volume.

21 🕸

Mutes the sound.

22 HOME MENU

TV/External Input mode: Displays the Menu screen.

23 AV SELECTION

Selects audio and video settings.

(AV mode: STANDARD, DYNAMIC, MOVIE, GAME, USER. PC mode: STANDARD, USER.)

24

TV/External input mode: Changes the wide screen size.

TELETEXT mode: Switches Teletext images. (full/upper half/lower half)

25 (

Selects the TELETEXT mode.

(all TV image, all TEXT image, TV/TEXT image)

26 **(ii**)

TELETEXT mode: Displays an Index page for the CEEFAX/FLOF format. Displays a TOP Over View page for the TOP format.

NOTE: When using the remote control unit, point it at the Plasma Display.

88

PDP-R06G

3

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2 3 4

A ■ Cleaning

• Before shipping out the product, be sure to clean the following positions by using the prescribed cleaning tools:

	Position to be cleaned	Cleaning tools	Remark
]	Fans	Cleaning paper : GED-008	Refer to "2.3 EXTERIOR SECTION", "7.1.2 DISASSEMBLY SECTION".